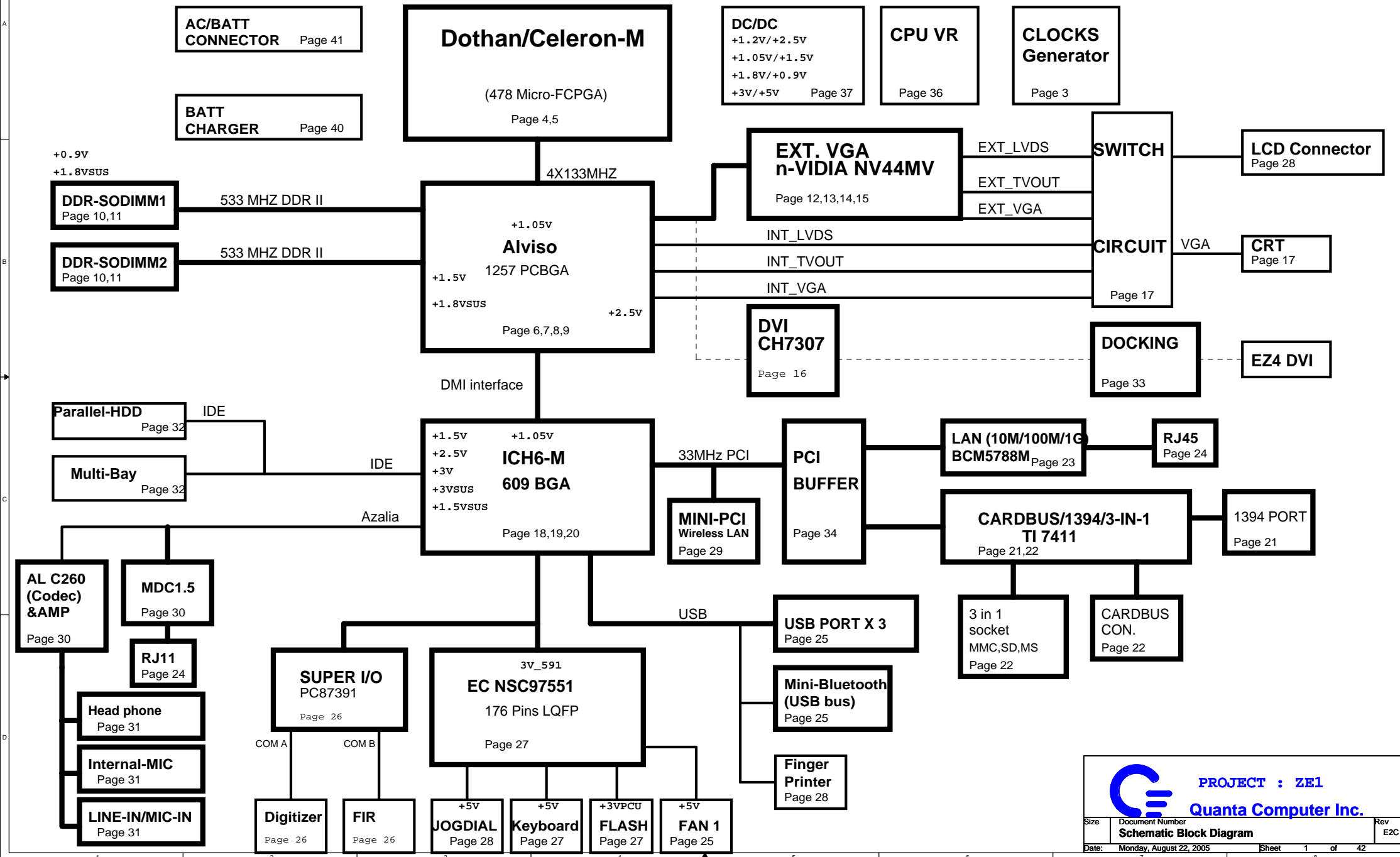


ZE1

REV: E2C



B TEST CHANGE ITEMS:

06/22 MODIFY LIST

1. Remove all no-use text, menu, i.e. Rev A2.
2. Page 12. Change R157, R158, R162~R167 power source from +3V to IFPC_IOVDD.
3. Page 15. Add R615, R616, R614 Q44 and Q43. Follow nVIDIA's suggestion. TMSD Backdrive Prevention circuit.
4. Page 16. Change signal I1 RN, R6, R7 to RNT15, R57 to RNT17, R58, R59 to RNT18, R60, R61 to RNT19. And use 0 ohm resistor.
5. Page 24. Swap net of CN10-12 and CN10-13. Wrong connection.
6. Page 25. Change USB power source from 5VPCU to +5VSUS. Wrong design.
7. Page 30. Reserve R617 (stuff), R618 (stuff), C859 (NA), C869 (NA), C861 (NA) and C862 (NA). For noise in EZ4's LINE-IN.

06/23 MODIFY LIST

8. Page 23: Delete R36 and R76. VESDChange PWR source from LAN_3V to 3V_LAN directly.
9. Page 23: Delete R40 from TCK.
10. Page 23: Delete R42 from O9-B and O9-C.
11. Page 23: Add option resistors in 3V_LAN. Add R56 and R57 for BCM4401.
12. Page 23: Add PI-filter for AVDD and separate from VDDP. Add L72.
13. Page 23: Add PI-filter for AVDDL. Add L73 and C863.
14. Page 23: BOM modification. Correct RDAC's resistor from 1.18K to 1.24K (BCM5788).
15. Page 23: BOM modification. R70: stuff: R68: NA. FAE: Support CLKRUN#.

	R51, R48	R56, R57	R38	R97, R87	Q5, Q9	U7	U8
BCM5798M	STUFF	NA	1.24K	STUFF	STUFF	STUFF	NA
BCM4401	NA	STUFF	1.27K	NA	NA	NA	STUFF
NOTE	+3VRUN	3V_LAN	RDAC			AT93C46	24LC128

06/28 MODIFY LIST

16. Page 28. Delete R112 (pull high/ 10K) and Add R624 (0 ohm) to GND. BOM.
17. Page 28. Add R625 and R626 to GND. ALPS setting. BOM.
18. Page 28. Add R619, R620, R621 and R623 for bypass 3D function. BOM.
19. Page 28. Reserve R624, R622, C864 and U62. Those are for 3D function.

06/28 MODIFY LIST

20. Page 13. Update and correct U27 and U28 footprint. Correct DQS and DQM.
21. Page 13. Re-route MDA[0..63], QSA[0..7] and -DQMA[0..7]
22. Page 9. C272 change from CH73301M8B9 to CH747LM8801 (LOW profile). BOM

B2 TEST CHANGE ITMES

07/19 MODIFY LIST

23. Page 25. Change U32, U58 and U33 to AL000547001 (GMT).
Change C489, C800 and C490 to CH71001M393 (100uF).
Del R289, R296, R562, R555, R292 and R294
24. Page 25. Add USB PWR control (SUSON) circuit. Add R554 (10K) and Q39(DTC144EUA)
25. Page 42. Add Semtech solution in Track-point.
Add U63, U66, U65, U64, C866, C868, C867, C870, C872, C871, R637, R638, R643, R642, R645, R644, R647, R641, R646, R640, R639, R638
26. Page 22. Chane CN13 from 4 in1 to 3 in 1. Re-routing it.
27. Page 28. BOM delete D4. Acer: no action in LID.

07/26 MODIFY LIST

28. Page 32. Add HOLE45 for MDC.
29. Page 32. Add HOLE46 for VRAM. (NO SPACE!!!)
30. Page 32. Add clip's PAD. P5, P6 and P7.

07/29 MODIFY LIST

31. Page 27. Add PULL HIGH 10K/ R649 to 3P. (EC)
32. Page 40. BOM: Remove R149 and Stuff P147. (EC)
33. Page 40. Delete P041, P109, PC97, PC98 and PC99. REF3V => VIN; REF3V => 3V_591. (EC and PWR)
34. Page 40. Update R120-1 and R130-101. REF3V => 1.2V, PR64-1, PR64-1 and P1U14-5.
35. Page 40. Delete PR134 and P1U10. Then SHORT them. (PWR)
36. Page 40. Change P0523-2 from P1U13-1(CC-SE) to P1U13-8. And change PR87 from 0 ohm to 10K. (PWR)
37. Page 28. Update CN7's footprint to 828264-06XX-6P-R
38. Page 28. Update CN8's footprint to 828264-10XX-10P-R
39. Page 28. Update CN3's footprint to 828264-04XX-4P-L
40. Page 39. Delete JP1. Change +1V5, ss5 to +3V_5S5.
40. Page 39. Delete JP2 and JP5. Change VCCP-OUT to +VCCP.
40. Page 31. Reserve and Stuff R650 and R651. For Fine-tune the value of headphone.

07/30 MODIFY LIST

41. Page 35. Disable NVDD2 + 1.2V for INT VGA cost-down
42. Page 35. change R128 from 100K to 97.6Kohm, R127 from 34K to 100Kohm for power fine-tune VGA voltage
43. Page 42. Change trackerpointer function from ALPS to SEMTECH
44. Page 12. Change EXT_BLON resistor R446 from 10K to 1K for boot white screen
45. Page 04. Disable R596 for Battery only can't boot

C TEST CHANGE ITEMS:

08/9 MODIFY LIST

- 46. Page 26. Modify serial port function for EZ4 (MRXD1 & MTXD1)
- 47. Page 25. Change USB_Power_on voltage from +5VSUS to 5VPCU
- 48. Page 25. Modify all USB power switch OC# pull-up 10k to +3VSUS

08/10 MODIFY LIST

50. Page 33. Remove LAN's reserve capacitors(C817/C513/C542/C546/C554/C555/C525/C523/C533/C530) by EMI engineer confirm

08/11 MODIFY LIST

52. Page 23. Add two new CAP 0.1UF(C873 & C874) for AVDD_LAN & +1.8V_1.2V_LAN by EMI request

08/12 MODIFY LIST

53. Page 23. Change R38 from 1.24k to 1.18k for LAN driver & template
54. Page 17. Change L59/L60/L61 from FBM-10-160808-470(470ohm) to BLM18BA220(220ohm) for EA measure fail
55. Page 28. Modify Bluetooth led can't light for FOXCONN Bluetooth module (Add R652 & R653)
56. Page 31. Modify Line-in noise change R650 & R651 from 0ohm to 10kohm
Page 30. Modify Line-in noise add C859 & C861 (0.1UF)
57. Page 31. Add AND gate(U67) & R654(0 ohm) for popo sound (delete D29 & D30 & R550)
58. Page 31. Add MOSFET (Q48&Q49) for headphone popo sound, and reserve Q46/Q47 for speaker popo sound issue
59. Page 33. Change L33/L34/L36 from 220ohm B26 to short

08/13 MODIFY LIST

60. Page 26. Change R449 from 1k ohm to 4.7k ohm for SIO

08/18 MODIFY LIST

61. Page 17. Change R399/R400/R401/R387/R388/R390 size from 0402 to 0603 for EMI request
62. Page 31. Add Q50 & R656(10K) for Normal close type audio jack issue (Reserve)
63. Page 32. Change Q13 from Transistor to MOS and R110 (47k to 10k) & C139(0.22U to 0.1U) and add R657 (0R) from EC (154pin) for Lite_On ODD issue
64. Page 33. Add C875/C876/C877/C878 (12pf) for Docking DVI EMI issue(Reserve)

08/19 MODIFY LIST

65. Page 21. Change 1394 connector(CN28) type from SMD to DIP for SMT request
66. Page 28. Update CN7's footprint to 88264-06XX-6P-R-ZE1 & CN8's footprint to 88264-10XX-10P-R-ZE1 for SMT request (Increase PAD length 0.2mm)
67. Page 25. Update CN5's footprint to SMO8B-SUR8-R-ZE1 for SMT request (Increase PAD length 0.2mm)
68. Page 41. Add new CAP(C171/P1C72) in BAT-V plane for EMI request
69. Page 23. Add new CAP(C879/C880) in AVDD0_LAN plane for EMI request
70. Page 23. Add new CAP(C881/C882) in +3V plane for EMI request
71. Page 33. Change C1/C2 from TOP to BOT side for Mechanical request

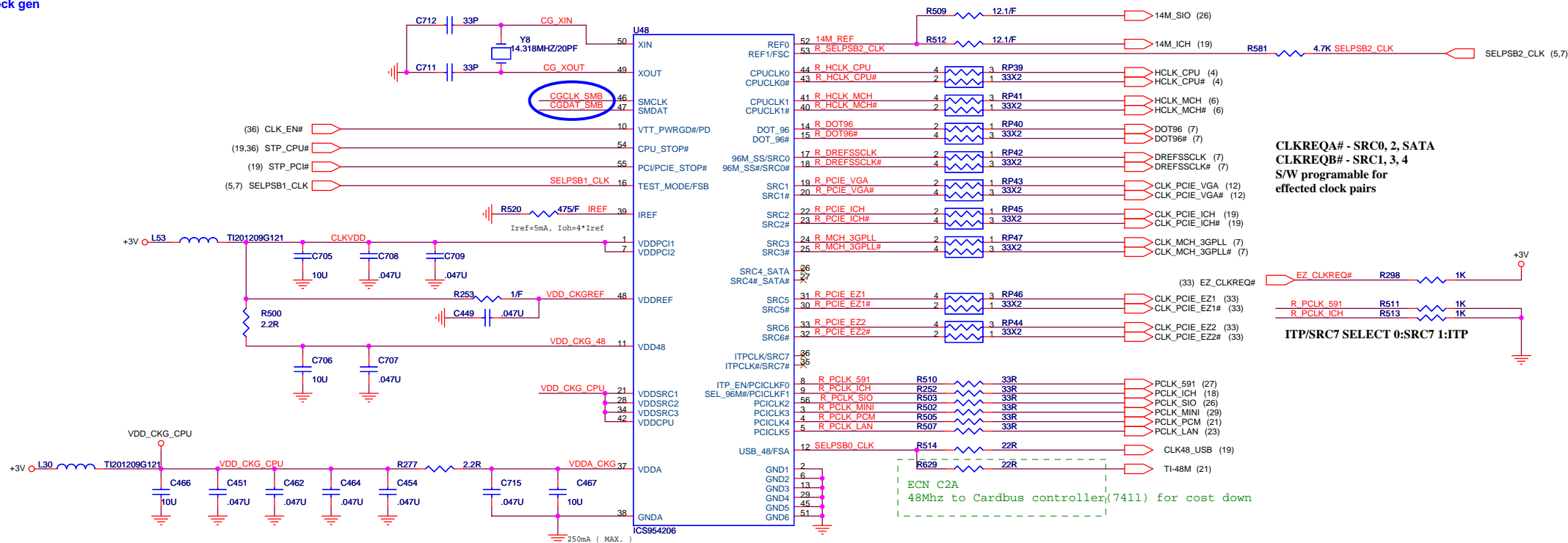
06/27 LAYOUT SWAP

- [illegible]

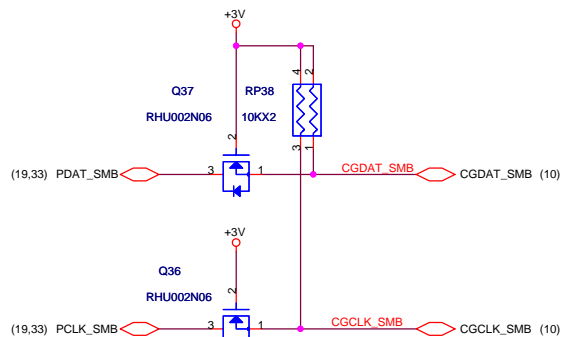


PROJECT : ZE1
Quanta Computer Inc

Clock gen



SM Bus



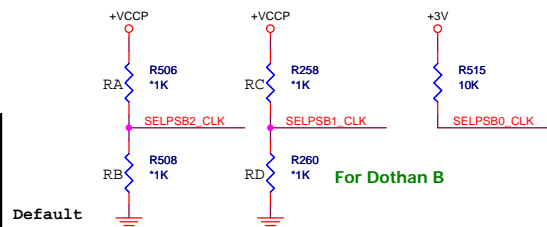
Speed setting

Resistor Stuff Table

	RA	RB	RC	RD
Dothan A 400	V	X	X	V
Dothan A 533	X	V	X	V
Dothan B	X	X	X	X

Clock Gen. Frequency Selection Table

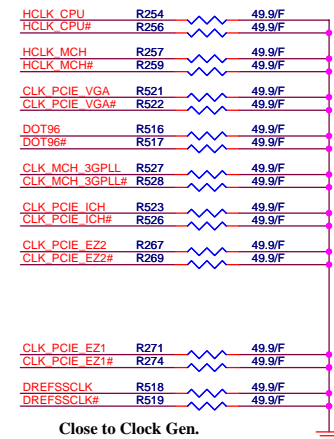
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33



DOTHAN BSEL Output Value

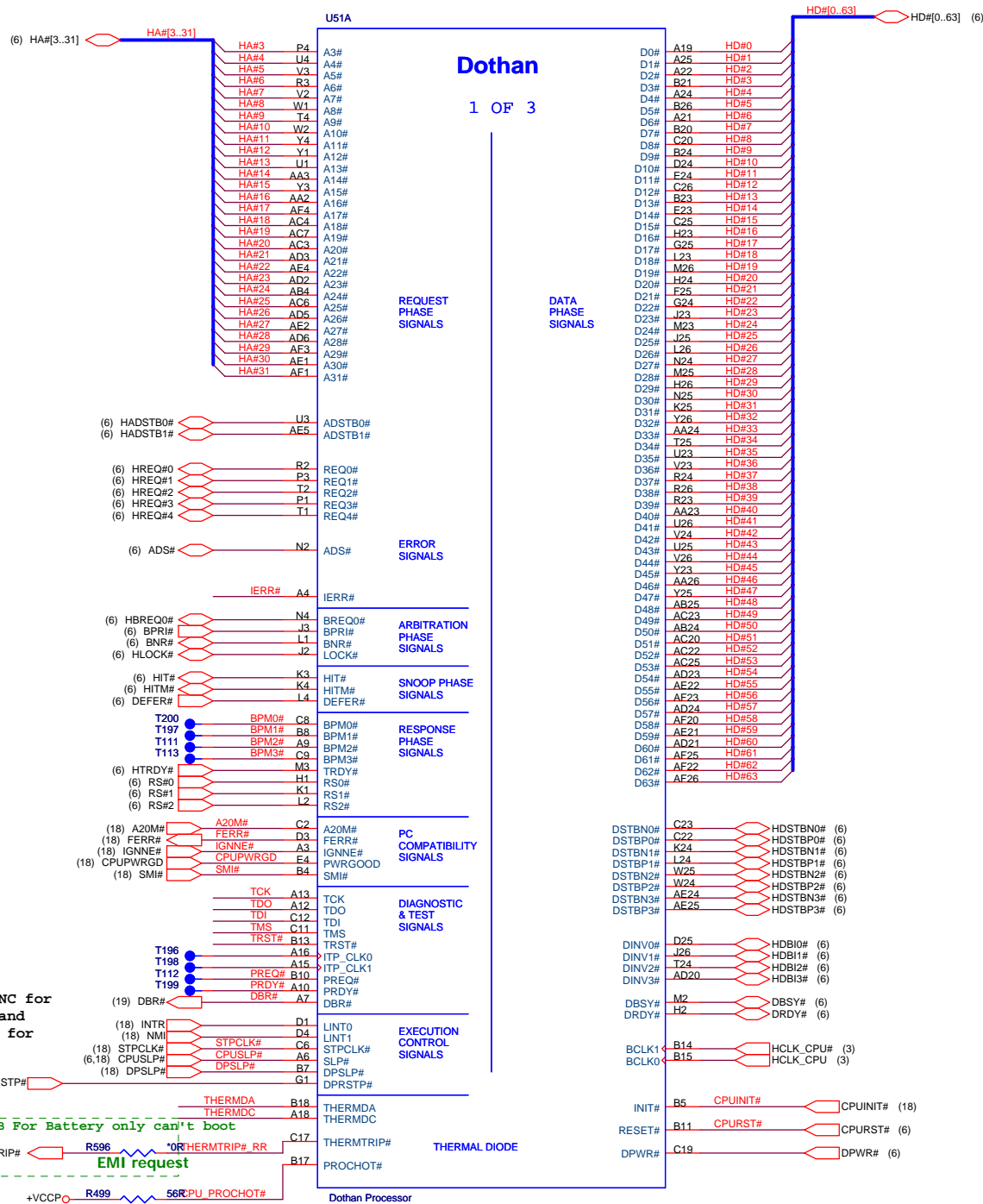
FSB Frequency	DO THAN A-Step		DO THAN B-Step	
	BSEL1	BSEL0	BSEL1	BSEL0
400 MHz	0	0	0	1
533 MHz	0	1	0	0

Clock terminator

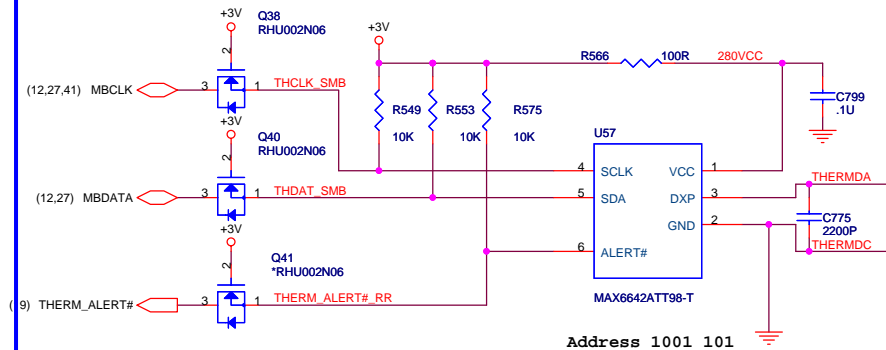


PROJECT : ZE1
Quanta Computer Inc.

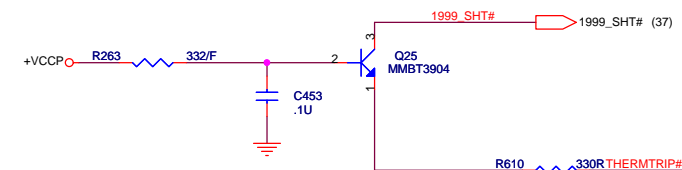
Size	Document Number Clock Gen.	Rev B2A
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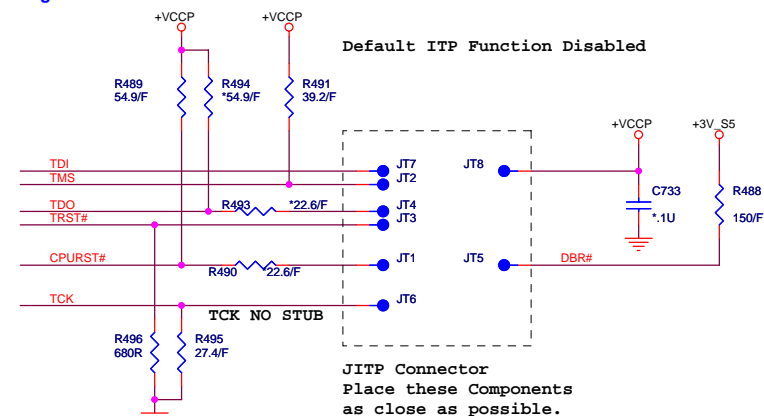
Thermal monitor



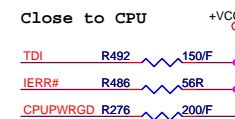
Power reset circuit



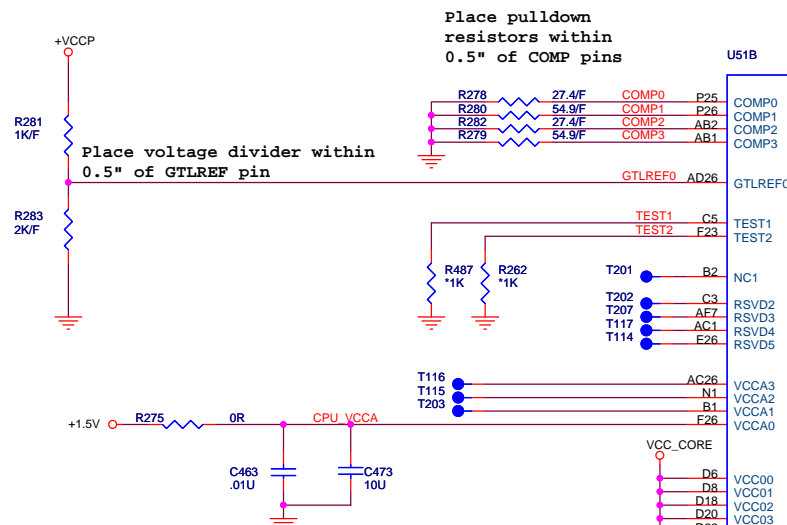
ITP debug



Close to CPU



PROJECT : ZE1
Quanta Computer Inc.

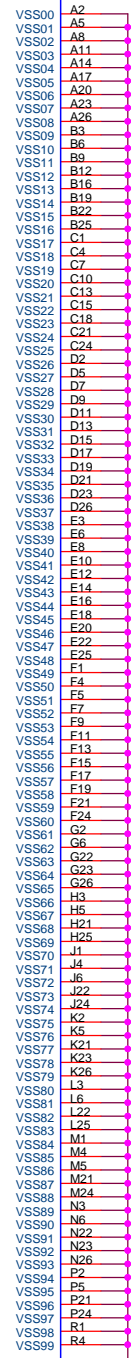


Dothan

2 OF 3

POWER, GROUND, RESERVED SIGNALS

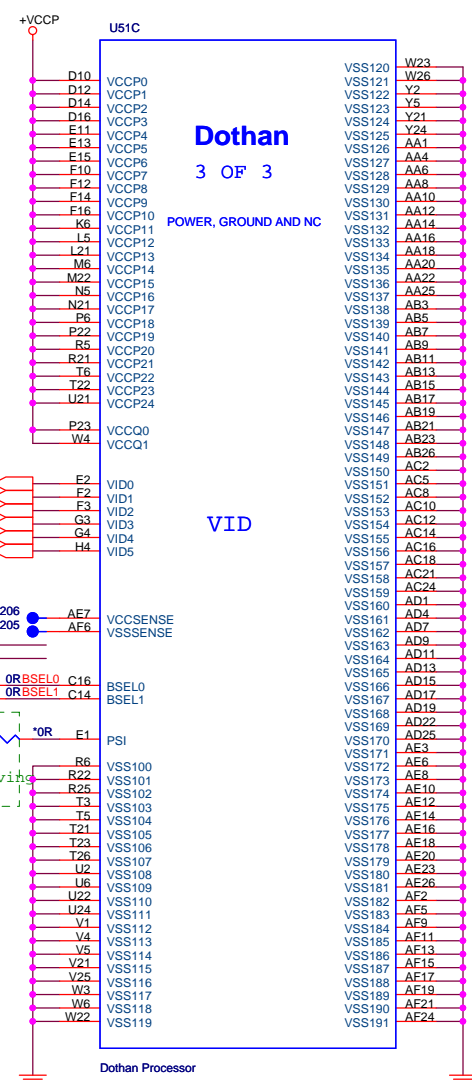
Dothan Processor

DOOTHAN-A NC
DOOTHAN-B POP(3.7) SELPSB2_CLK
(3.7) SELPSB1_CLKSELPSB2_CLK R498
SELPSB1_CLK R497SELPSB2_CLK R498
SELPSB1_CLK R497

(3.6) PSI#

ECN C2A

Reserve for Power saving



Dothan

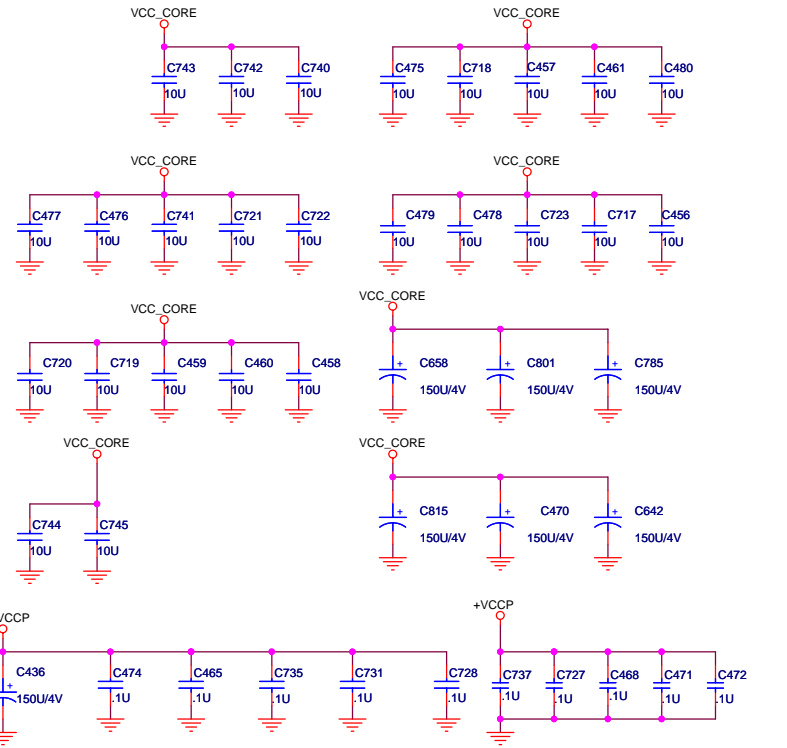
3 OF 3

POWER, GROUND AND NC

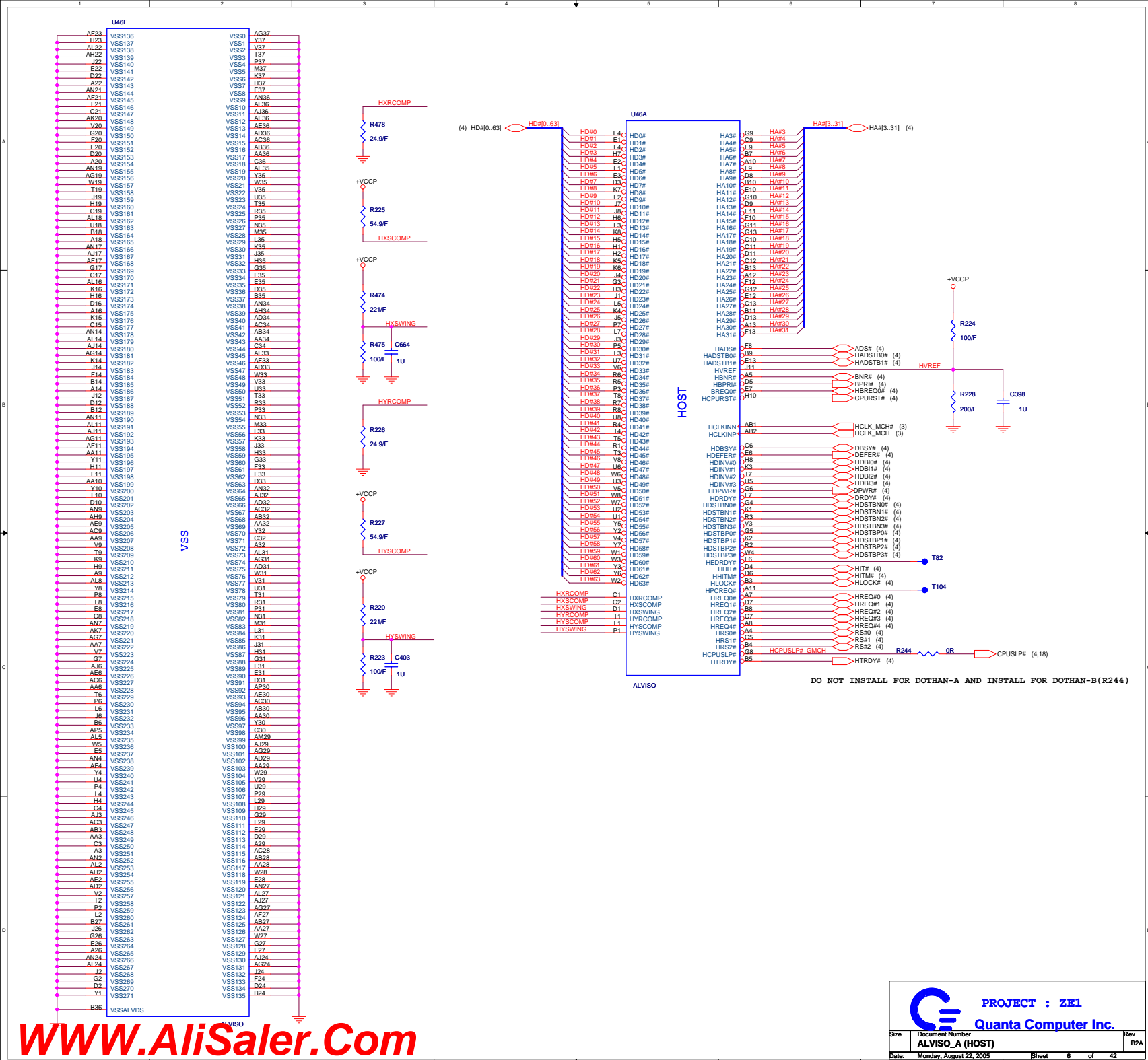
VID

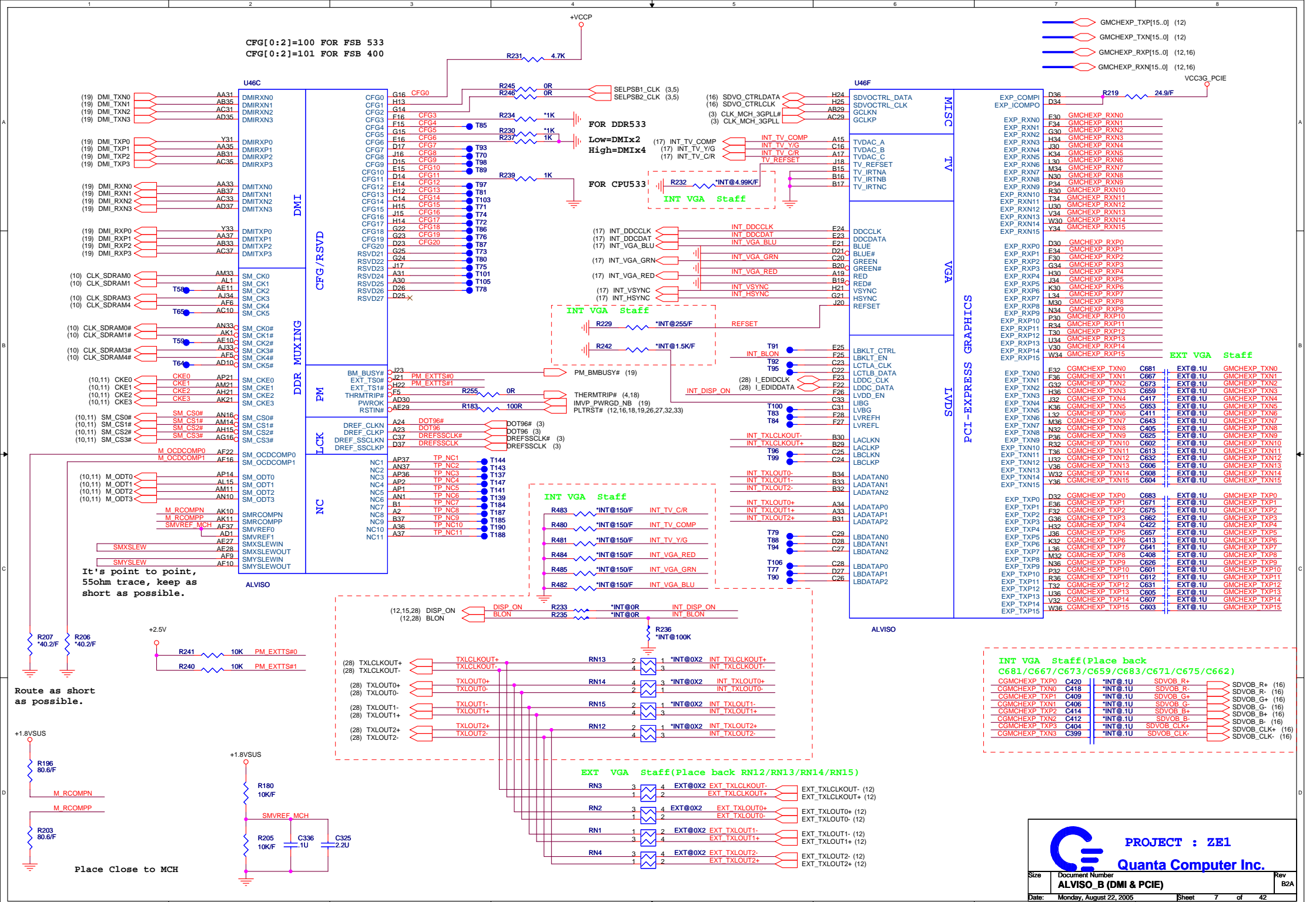
Dothan Processor

CAP



PROJECT : ZE1
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(10) R_A_MD[0..63]



DDR SYSTEM MEMORY A

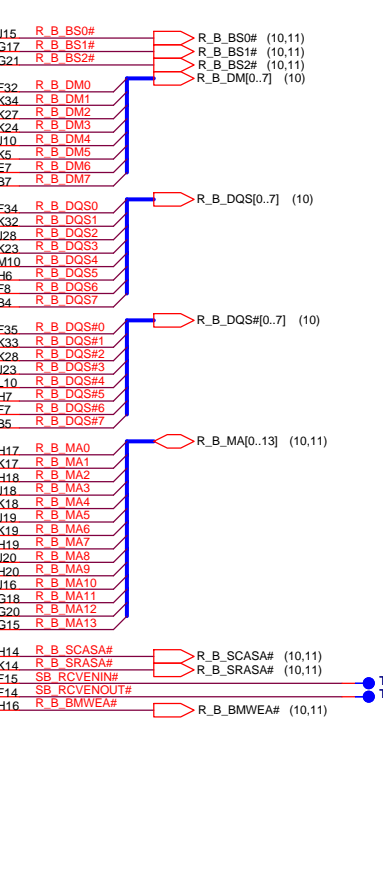
ALVISO

(10) R_B_MD[0..63]

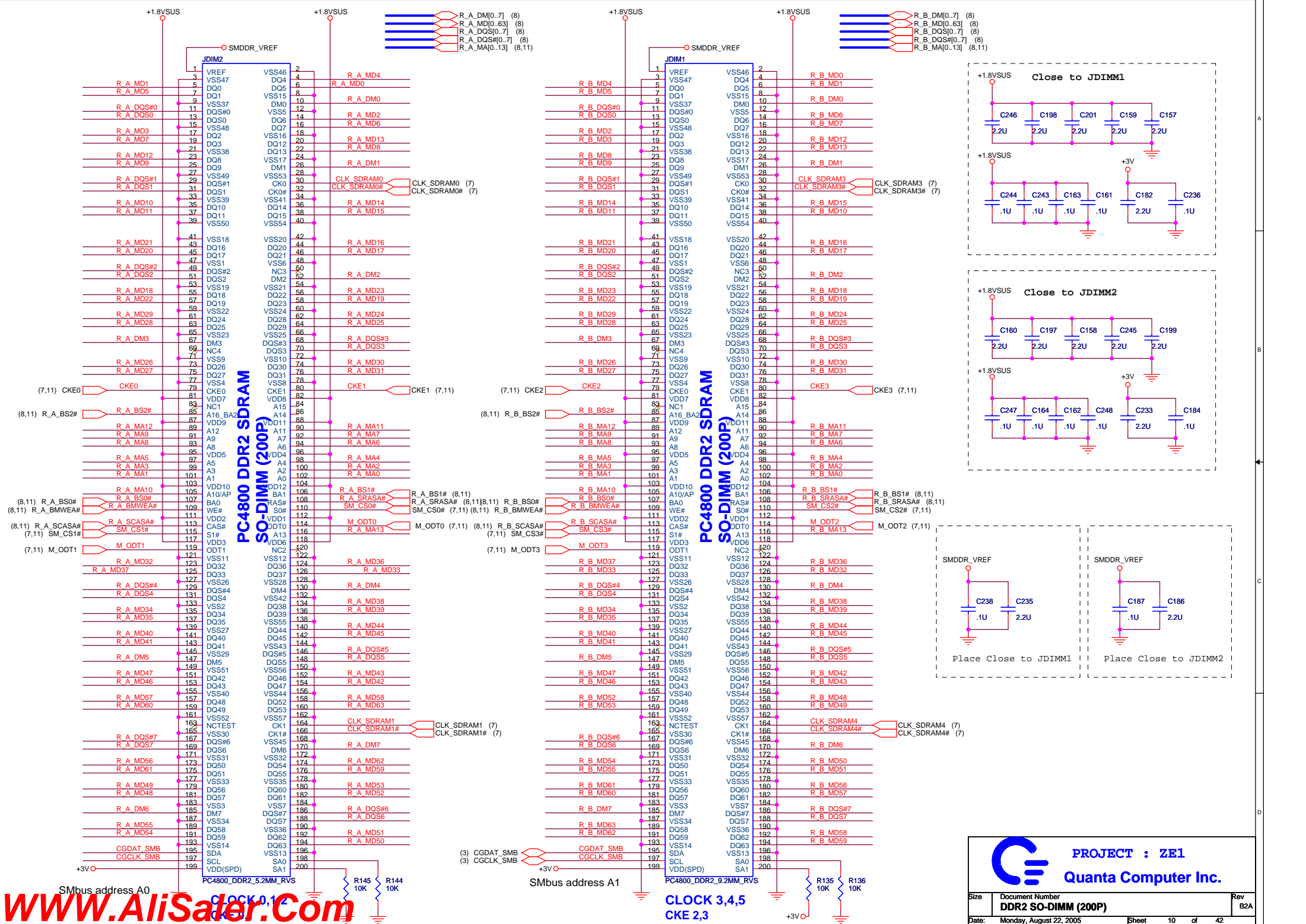


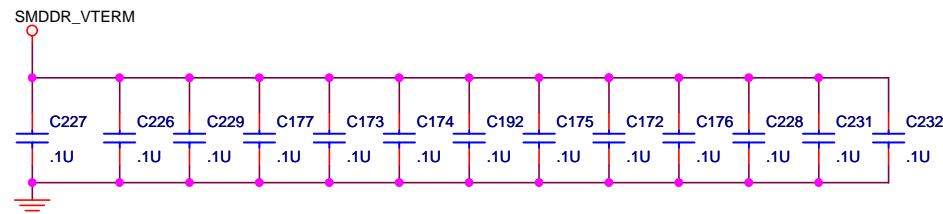
DDR SYSTEM MEMORY B

ALVISO

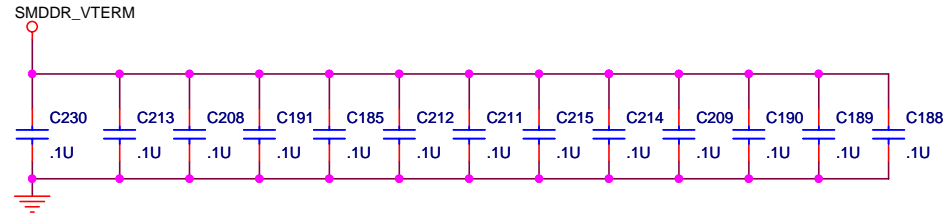


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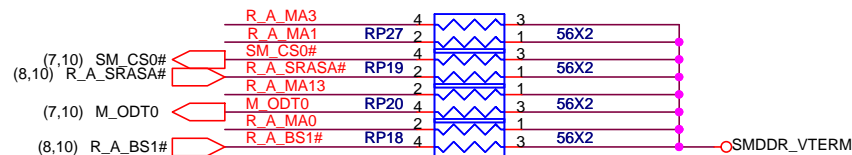
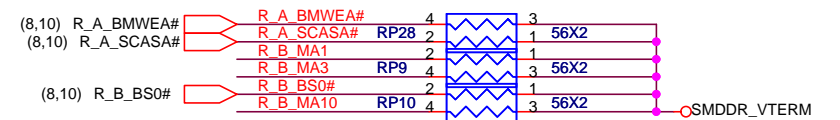
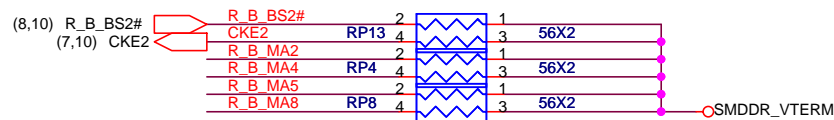
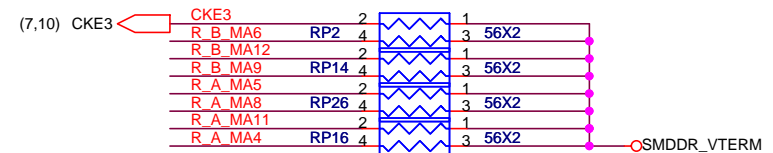
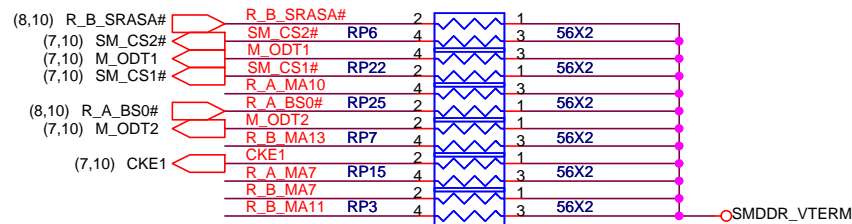
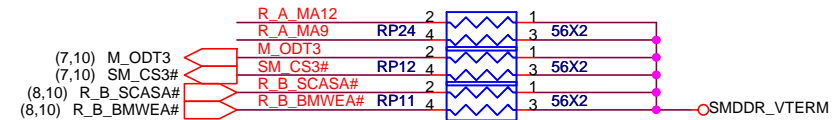
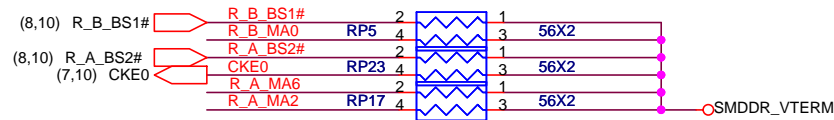
Layout note: Place one cap close to every 2 pullup resistors terminated to SMDRR_VTERM

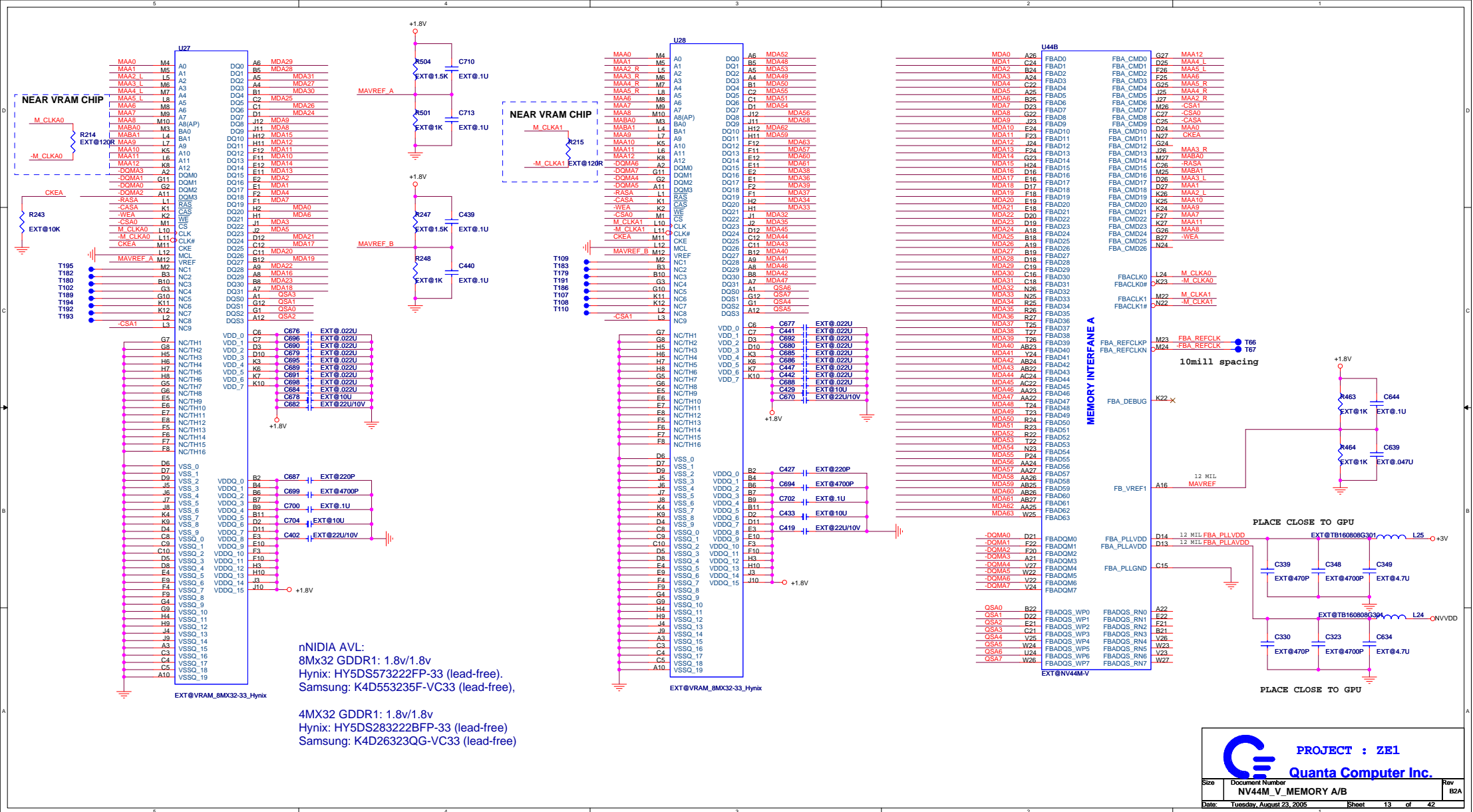


Layout note: Place one cap close to every 2 pullup resistors terminated to SMDRR_VTERM

R_A_MA[0..13] (8,10)

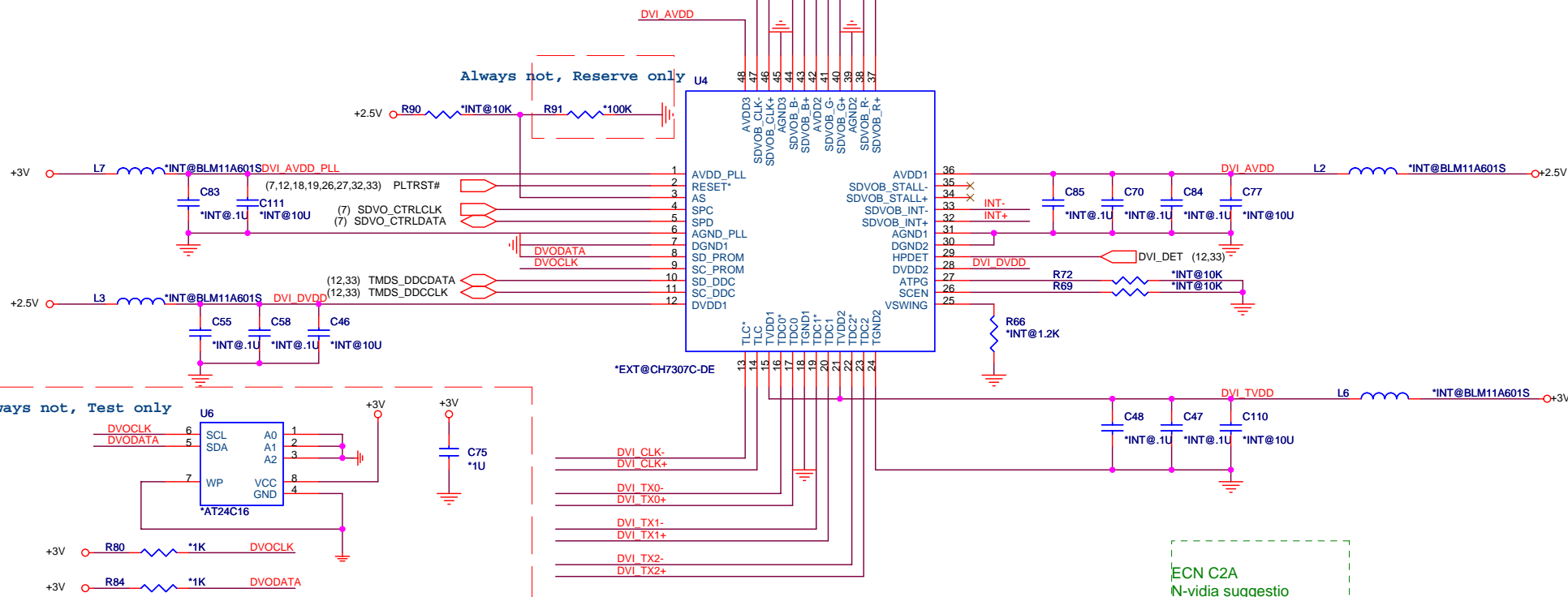
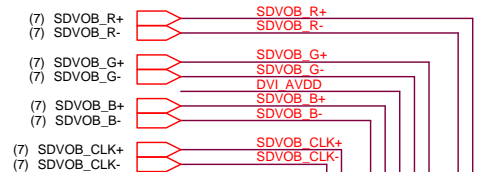
R_B_MA[0..13] (8,10)





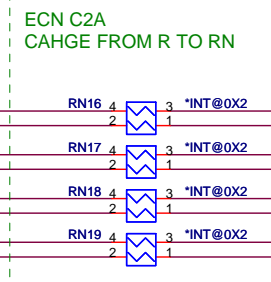
DVI control schematic(INT VGA USE)

INT VGA Staff

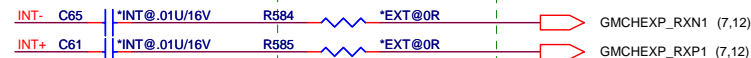
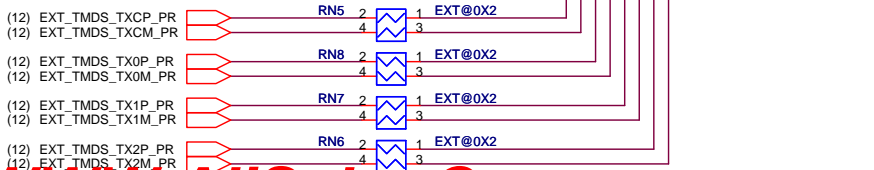


DVI Output select

INT VGA Staff



EXT VGA Staff(Default)



PULL Low For DVO Not Present(Internal PULL LOW In 915GM)

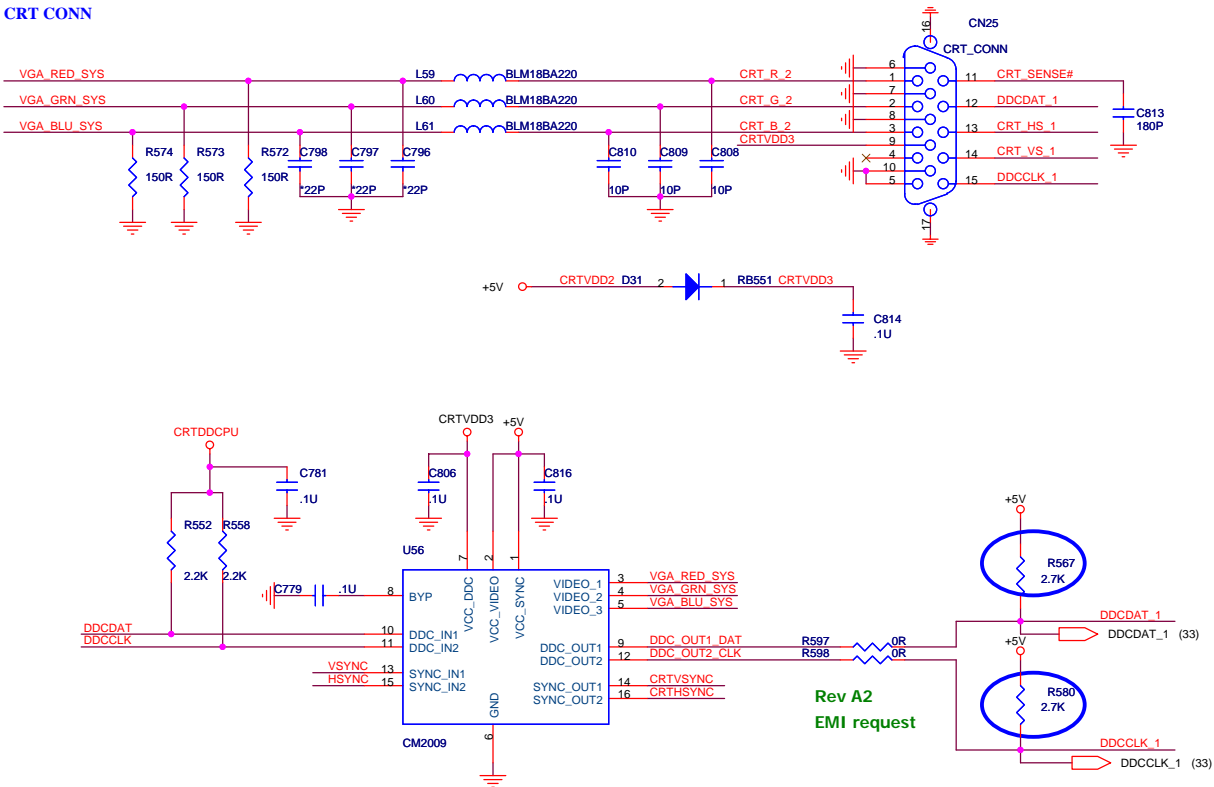
+2.5V 250mA
+3V 190mA



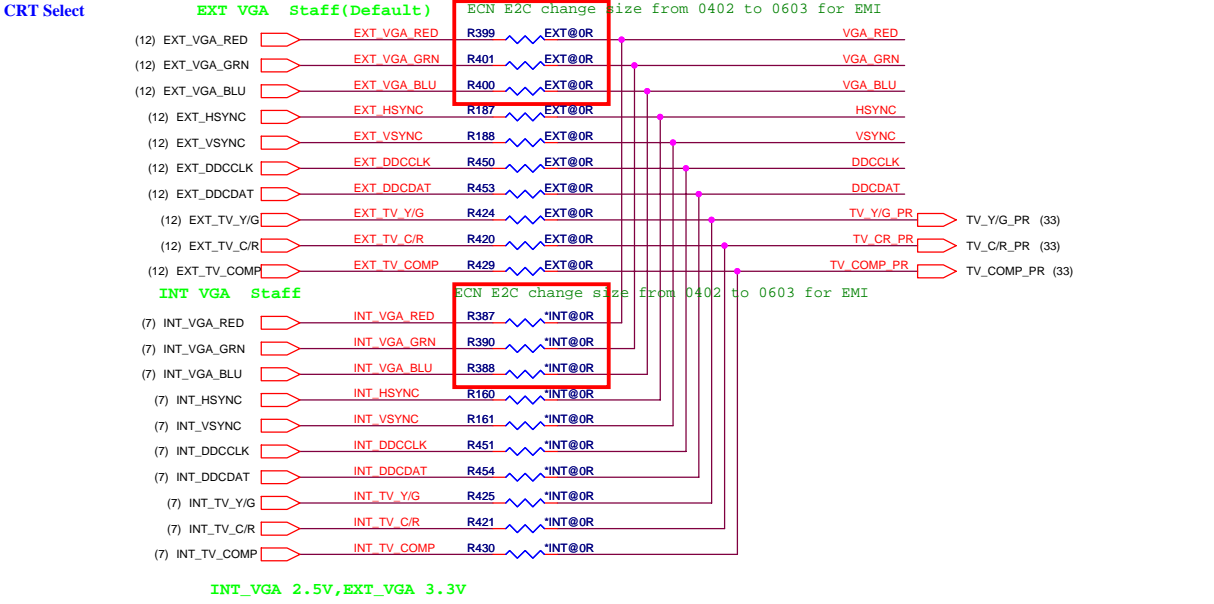
PROJECT : ZE1
Quanta Computer Inc.

Size	Document Number	Rev
	INT. DVI	B2A
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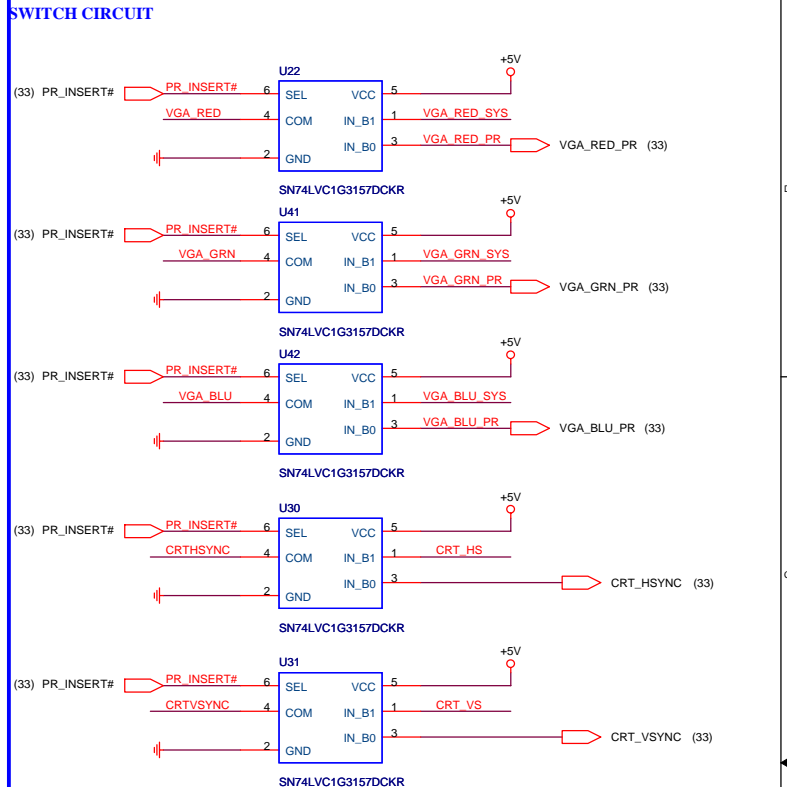
CRT CONN



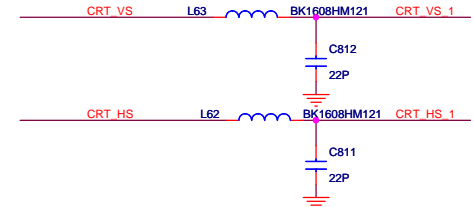
CRT Select



SWITCH CIRCUIT

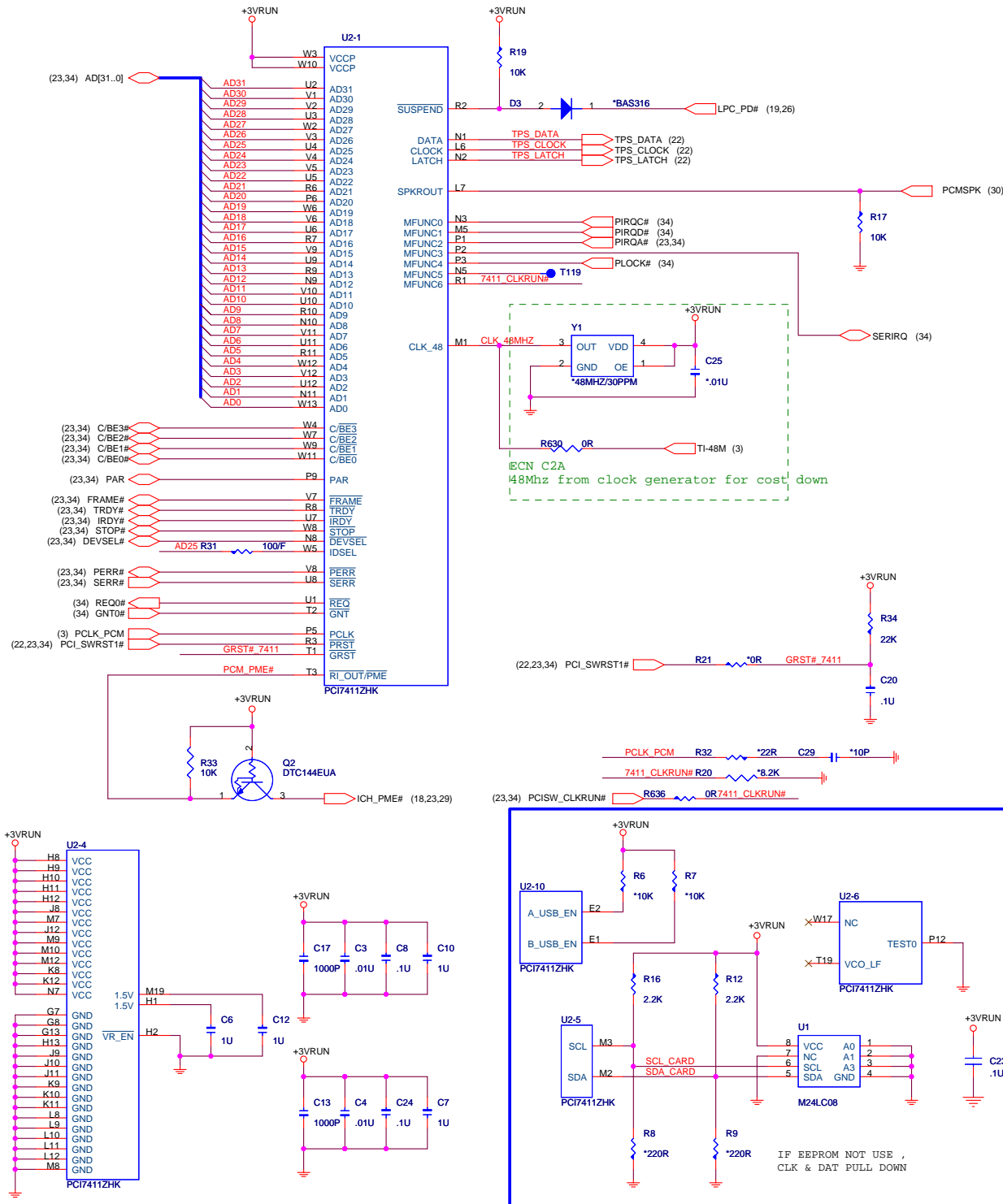


Place each one capacitor for each one switch

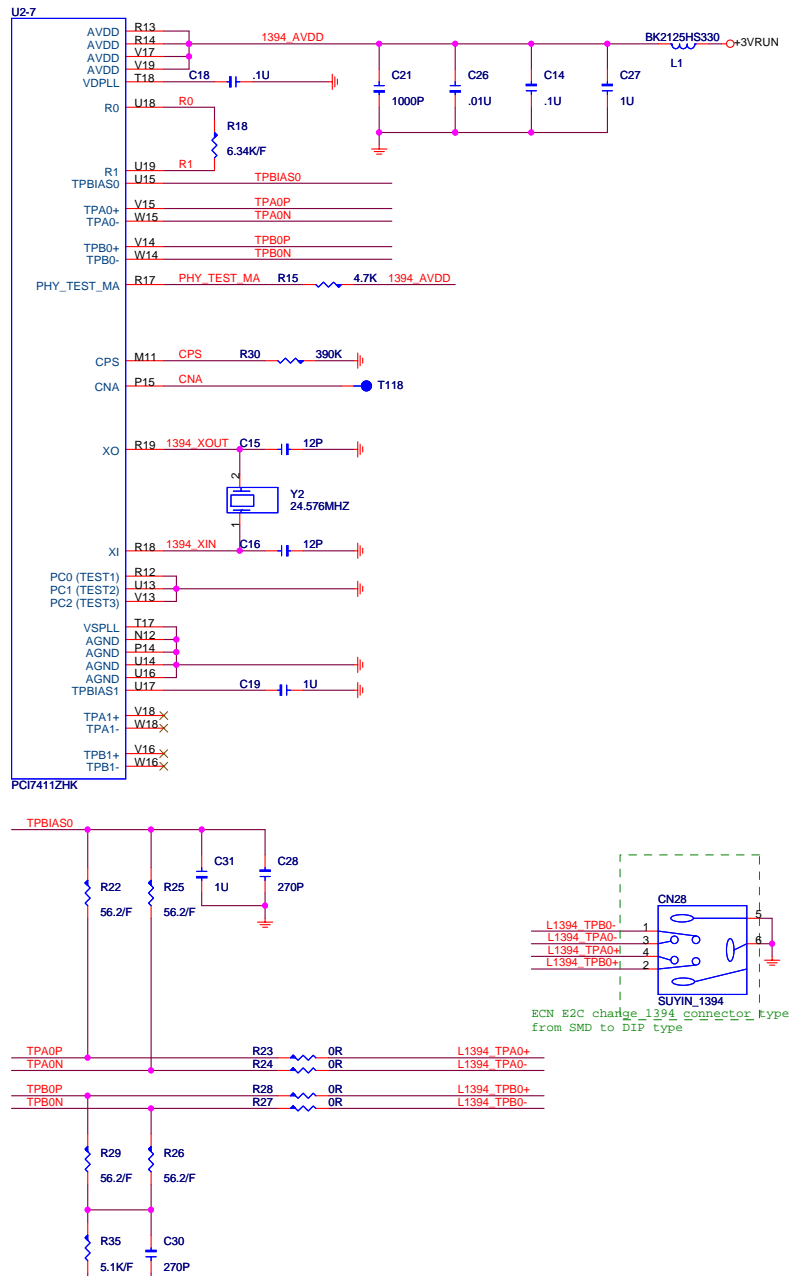


Size	Document Number ICH6-M (CPU, PCI, IDE)	Rev B2A
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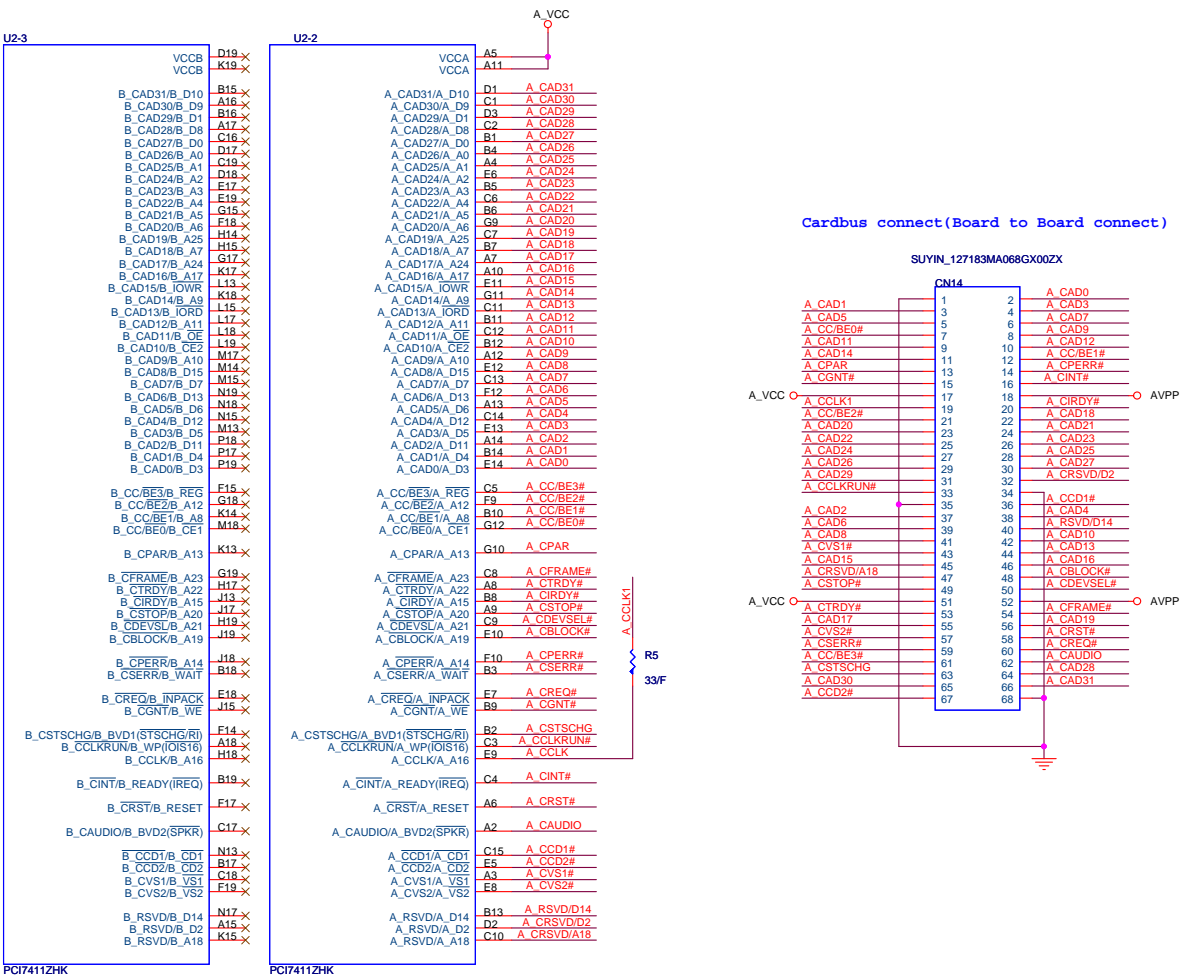
7411 PCI Interface



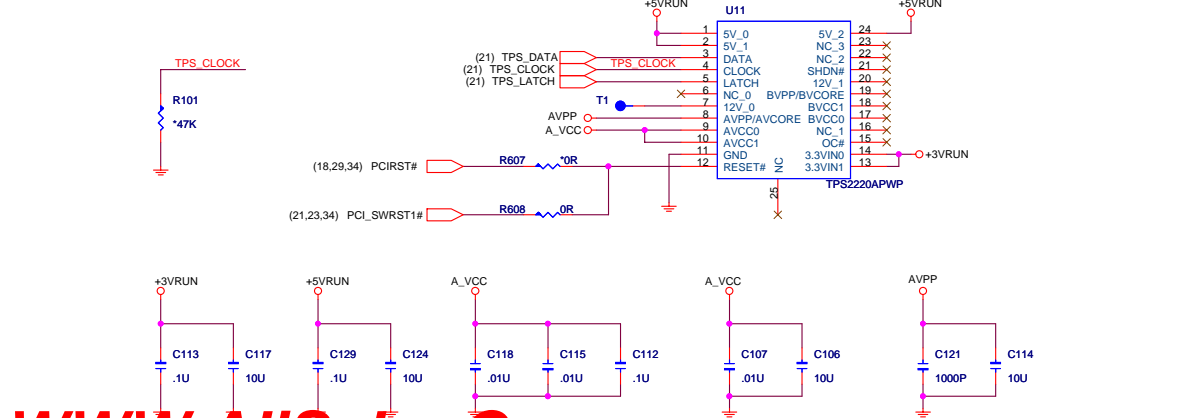
1394 Interface(CONN/EEPROM)



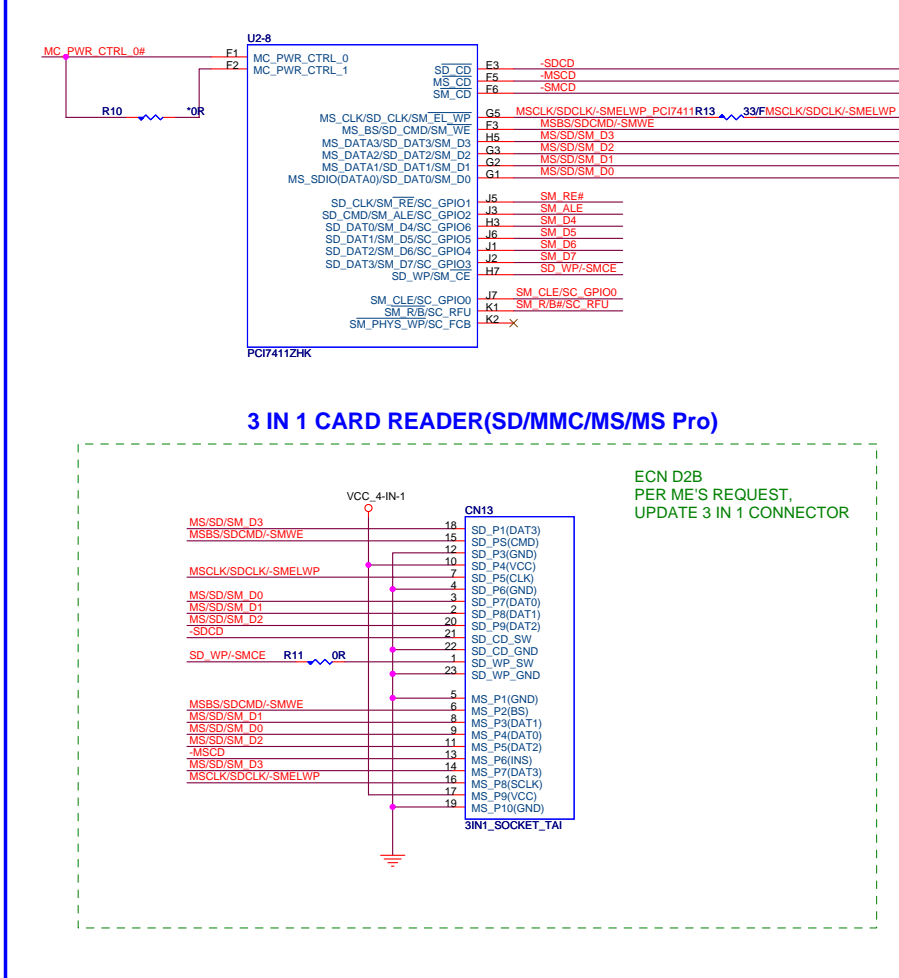
Cardbus Interface



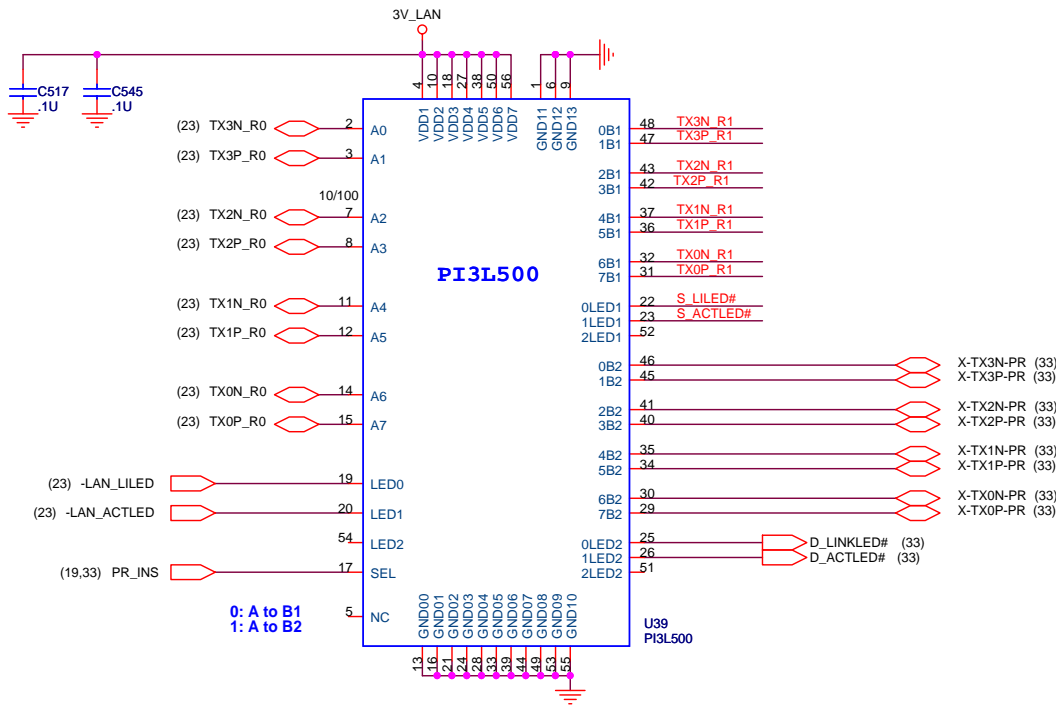
Cardbus Power



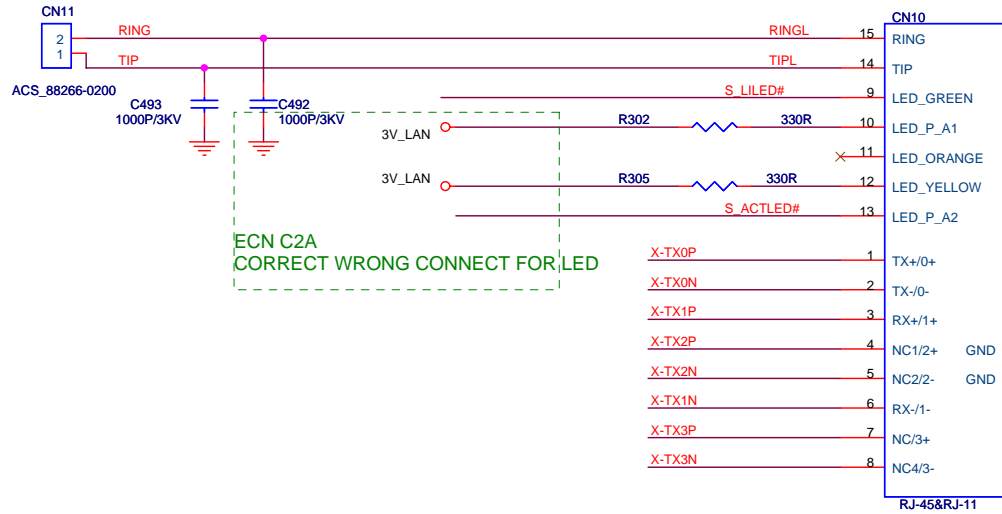
Card Reader Interface



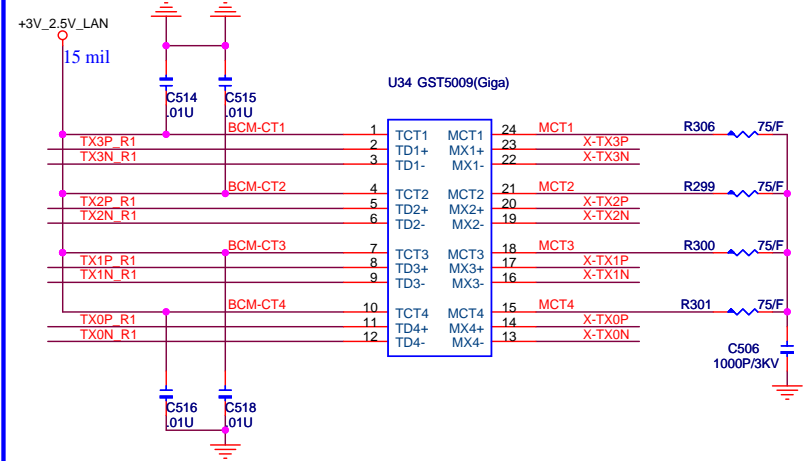
LAN Switch



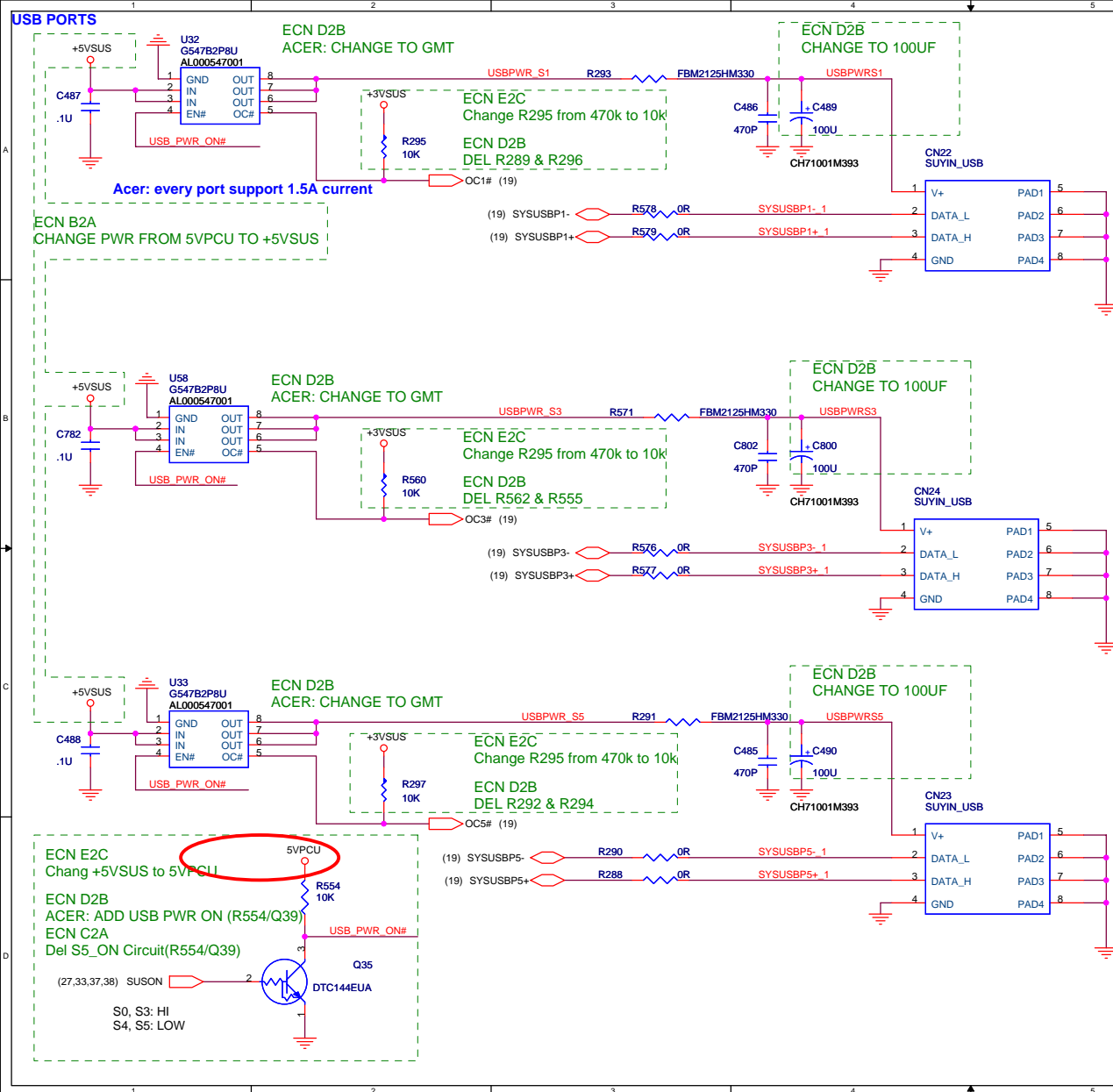
Modem conn and LAN connect



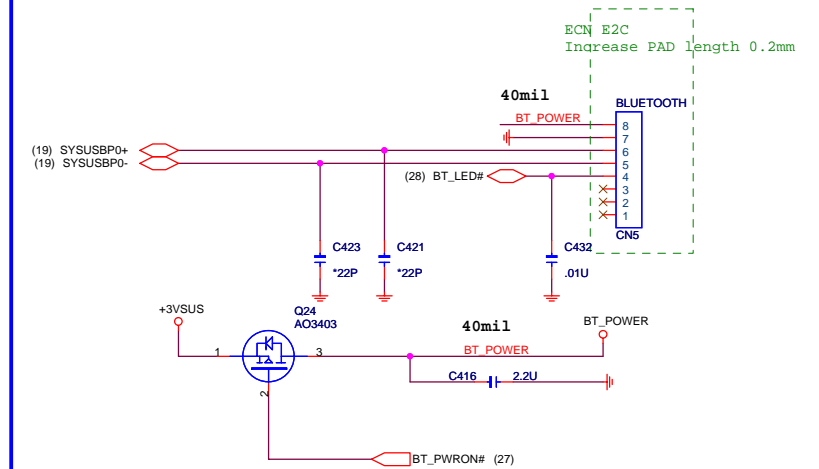
LAN transformer



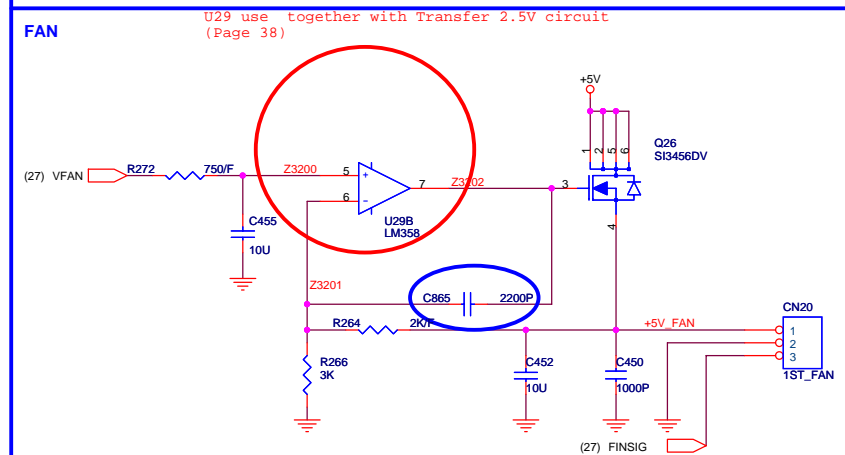
PROJECT : ZE1
Quanta Computer Inc.



Bluetooth



FAN



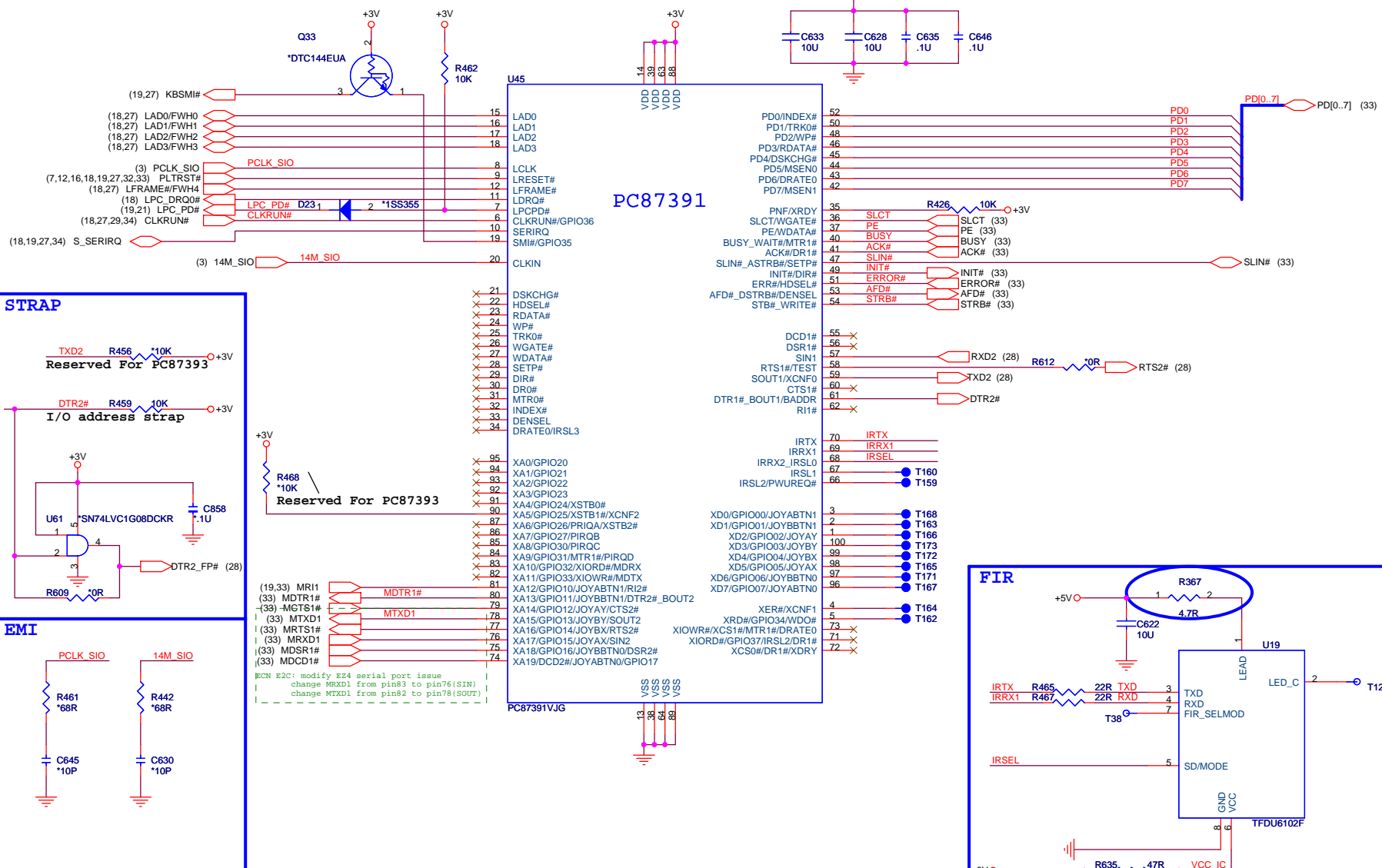
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Quanta Computer Inc.

Size	Document Number	Rev
		D2B

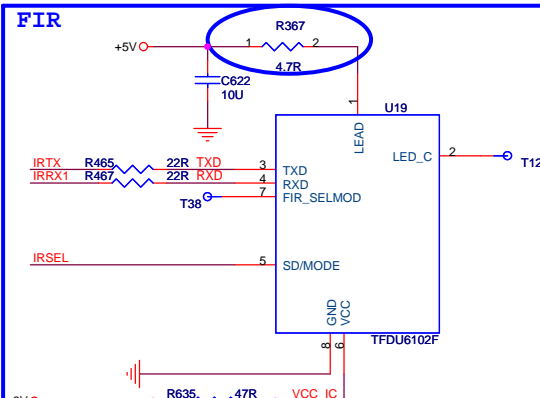
USB PORTS & BLUETOOTH & FAN


Date: Monday, August 22, 2005 Sheet 25 of 42

SIO(LPT/Serial port/Digitizer/FIR)



WWW.AliSaler.Com



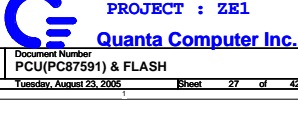
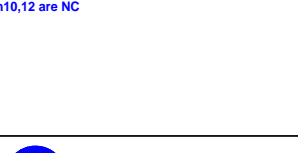
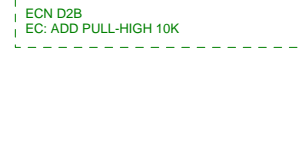
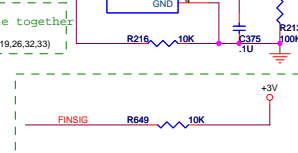
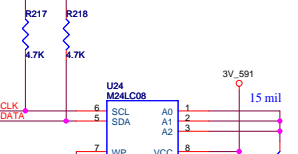
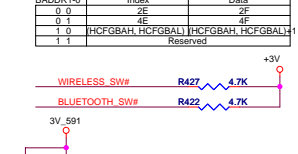
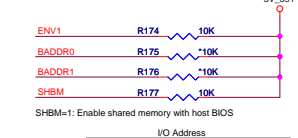
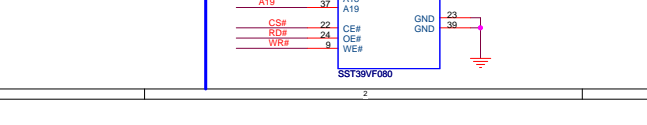
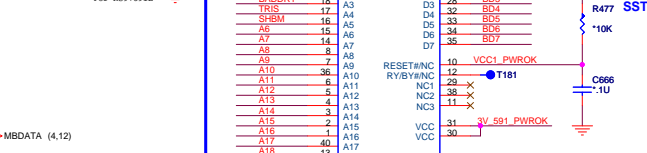
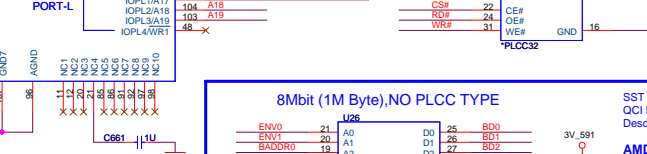
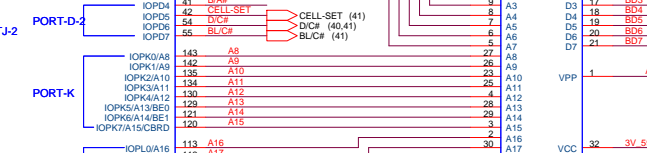
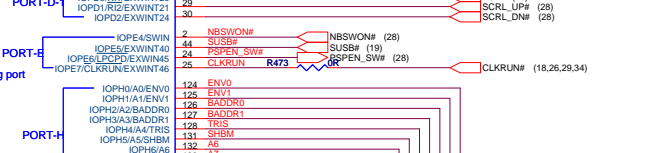
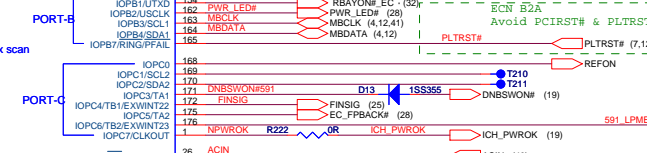
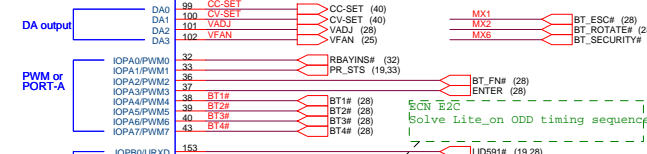
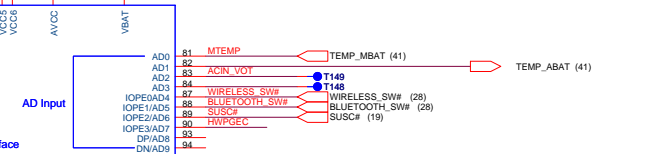
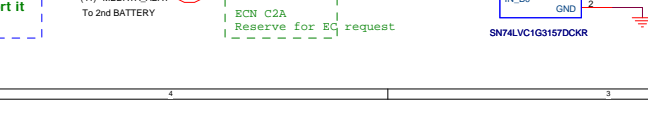
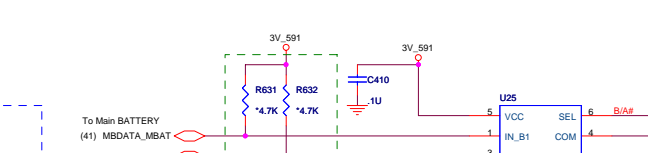
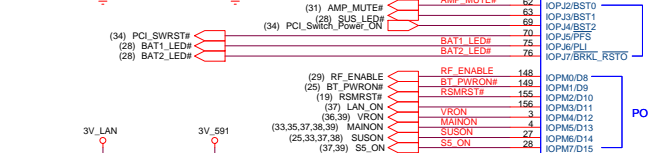
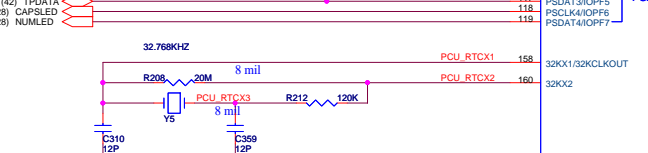
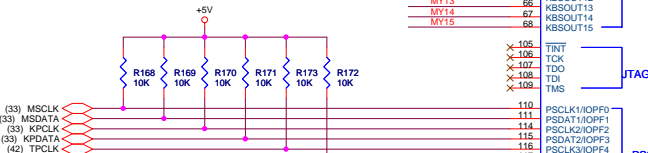
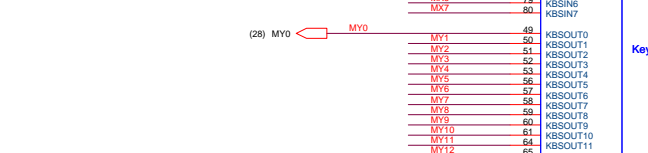
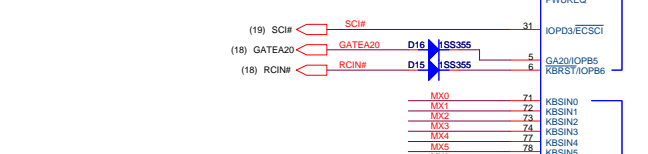
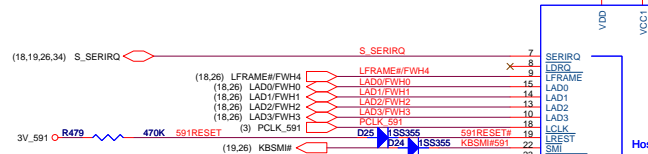
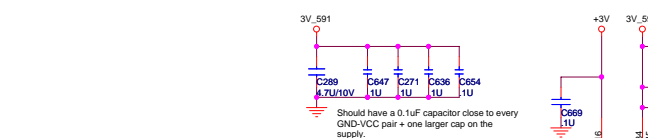
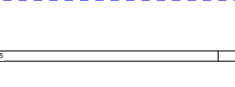
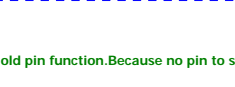
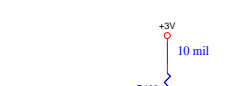
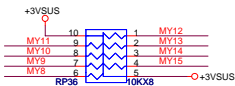
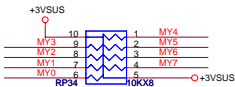
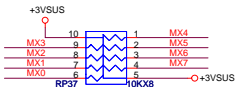


PROJECT : ZE1
Quanta Computer Inc.

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	PC87391, FIR, DIGITIZER	B2A
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EC suggestion for S3 wake-up change power from 5VPCU to +3VSUS

KEYBOARD



I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	HCFGBAH, HCFGGBAL, HCFGGBAH, HCFGGBAL	1
1 1	Reserved	

SHBM=1: Enable shared memory with host BIOS

WIRELESS_SW# R427 4.7K

BLUETOOTH_SW# R422 4.7K

3V_591

15 mil

R215 10K

3V_591

R449 10K

3V_591

10K

3V_591

10K

3V_591

10K

3V_591

10K

3V_591

10K

3V_591

10K

I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	HCFGBAH, HCFGGBAL, HCFGGBAH, HCFGGBAL	1
1 1	Reserved	

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3V_591

10K

3V_591

10K

3V_591

10K

I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	HCFGBAH, HCFGGBAL, HCFGGBAH, HCFGGBAL	1
1 1	Reserved	

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3V_591

10K

3V_591

10K

3V_591

10K

I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	HCFGBAH, HCFGGBAL, HCFGGBAH, HCFGGBAL	1
1 1	Reserved	

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BLUETOOTH_SW# R422 4.7K

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3V_591

10K

3V_591

10K

3V_591

10K

I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	HCFGBAH, HCFGGBAL, HCFGGBAH, HCFGGBAL	1
1 1	Reserved	

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BLUETOOTH_SW# R422 4.7K

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3V_591

10K

3V_591

10K

3V_591

10K

I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	HCFGBAH, HCFGGBAL, HCFGGBAH, HCFGGBAL	1
1 1	Reserved	

SHBM=1: Enable shared memory with host BIOS

WIRELESS_SW# R427 4.7K

BLUETOOTH_SW# R422 4.7K

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R215 10K

3V_591

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3V_591

10K

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3V_591

10K

3V_591

10K

3V_591

10K

3V_591

10K

I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	HCFGBAH, HCFGGBAL, HCFGGBAH, HCFGGBAL	1
1 1	Reserved	

SHBM=1: Enable shared memory with host BIOS

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BLUETOOTH_SW# R422 4.7K

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R215 10K

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R449 10K

3V_591

10K

3V_591

10K

3V_591

10K

3V_591

10K

3V_591

10K

3V_591

10K

I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	HCFGBAH, HCFGGBAL, HCFGGBAH, HCFGGBAL	1
1 1	Reserved	

SHBM=1: Enable shared memory with host BIOS

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BLUETOOTH_SW# R422 4.7K

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15 mil

R215 10K

3V_591

R449 10K

3V_591

10K

3V_591

10K

3V_591

10K

3V_591

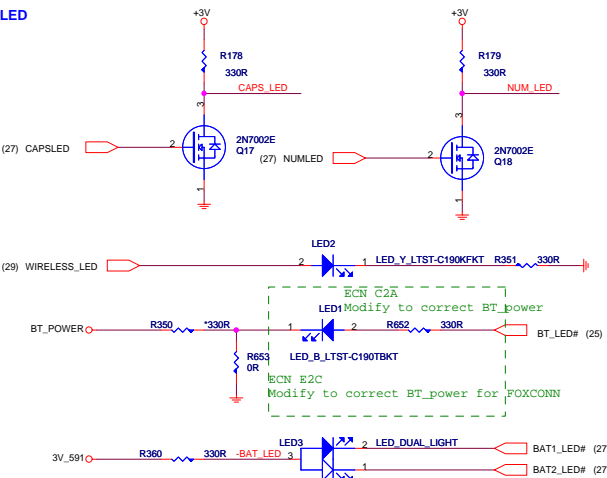
10K

3V_591

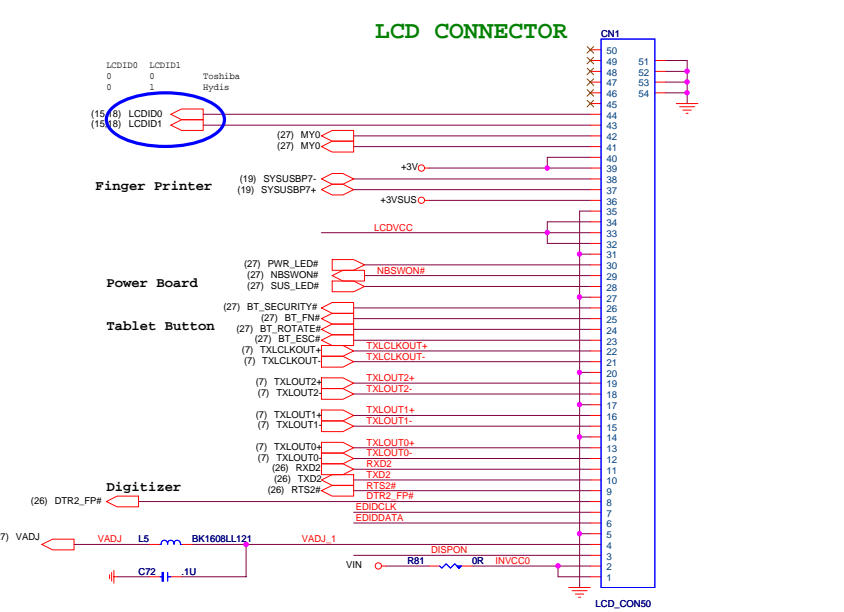
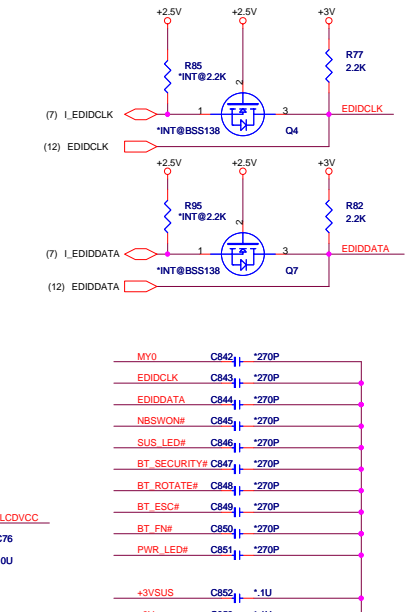
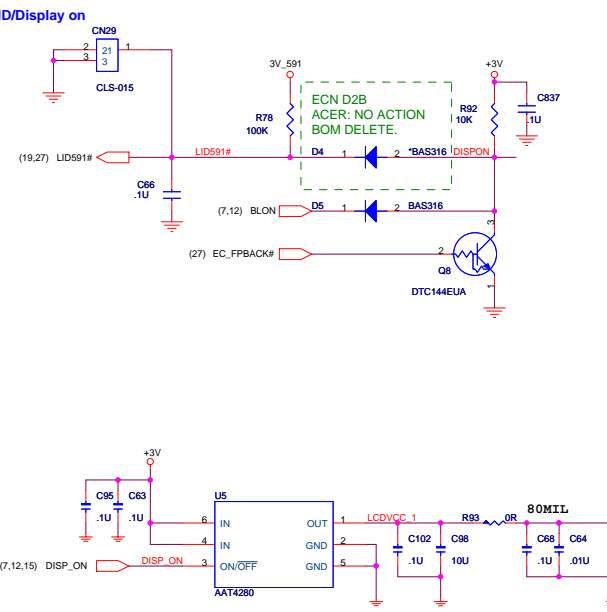
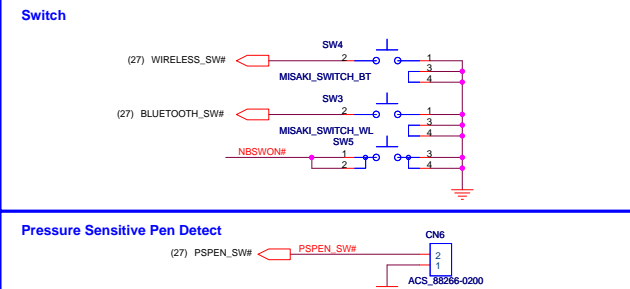
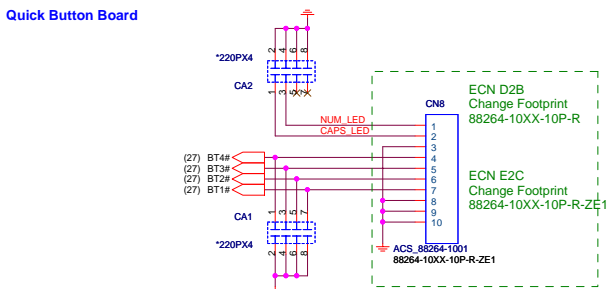
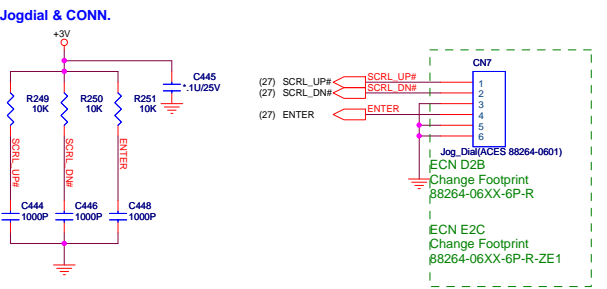
10K

3V_591

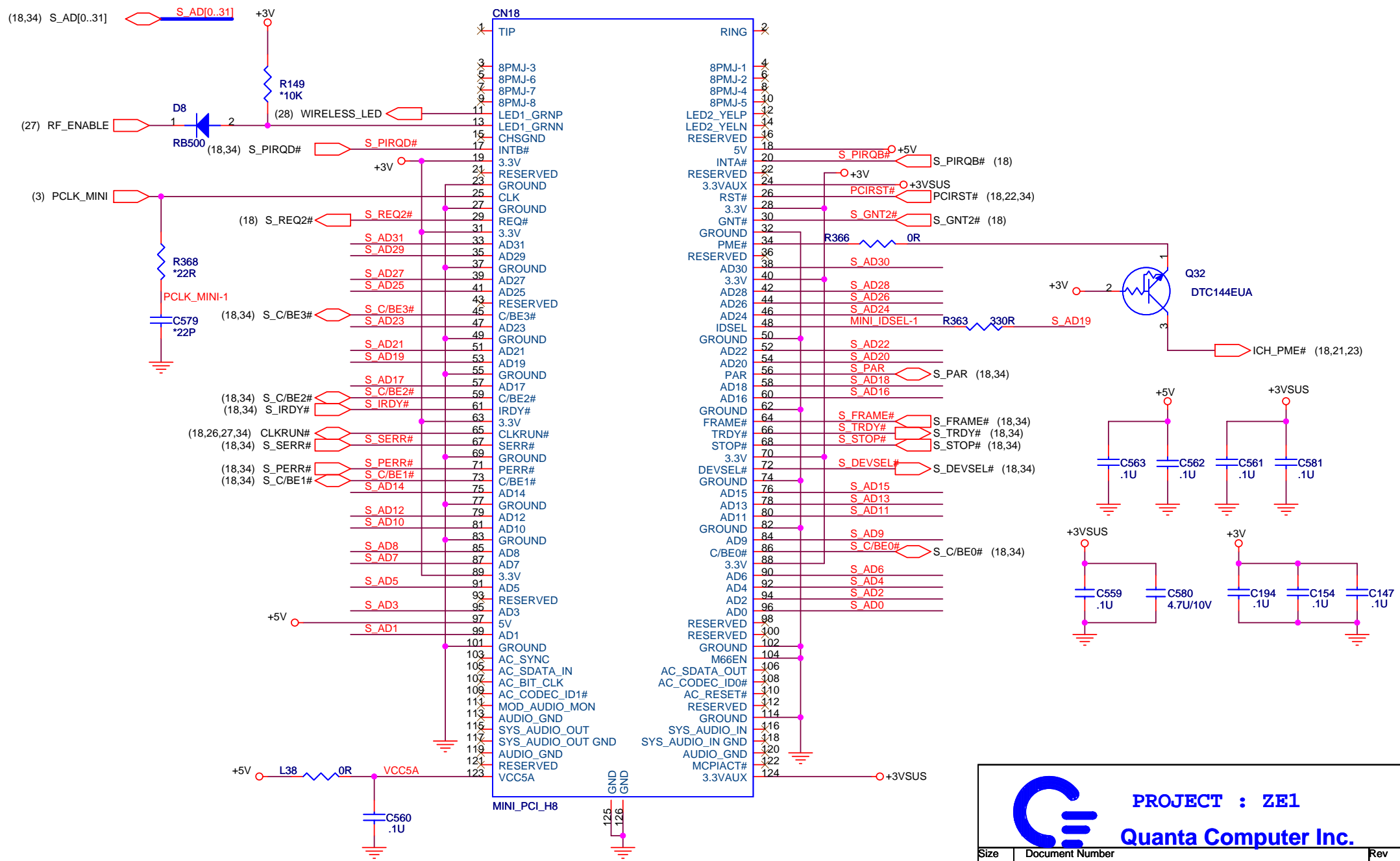
10K



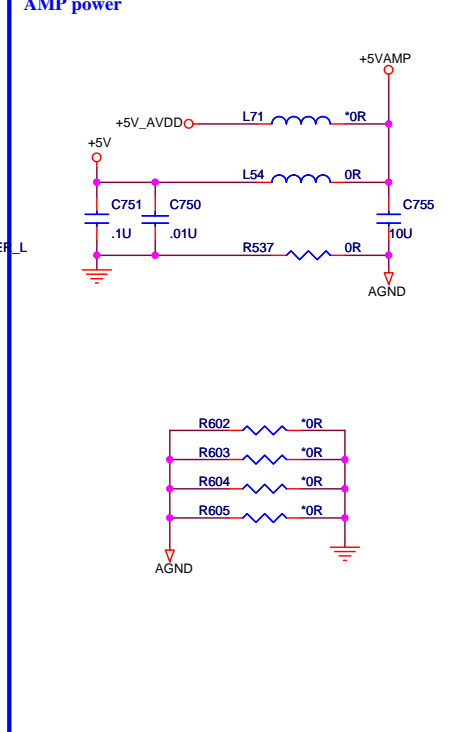
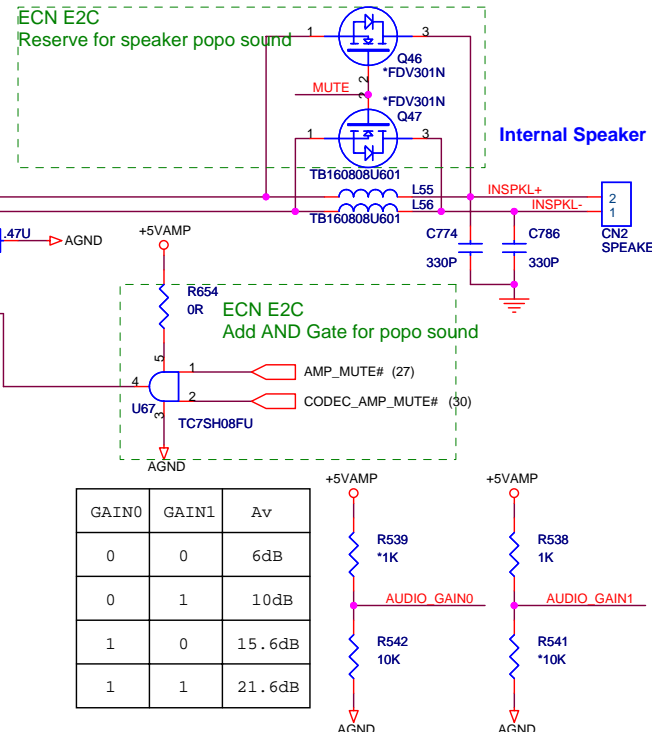
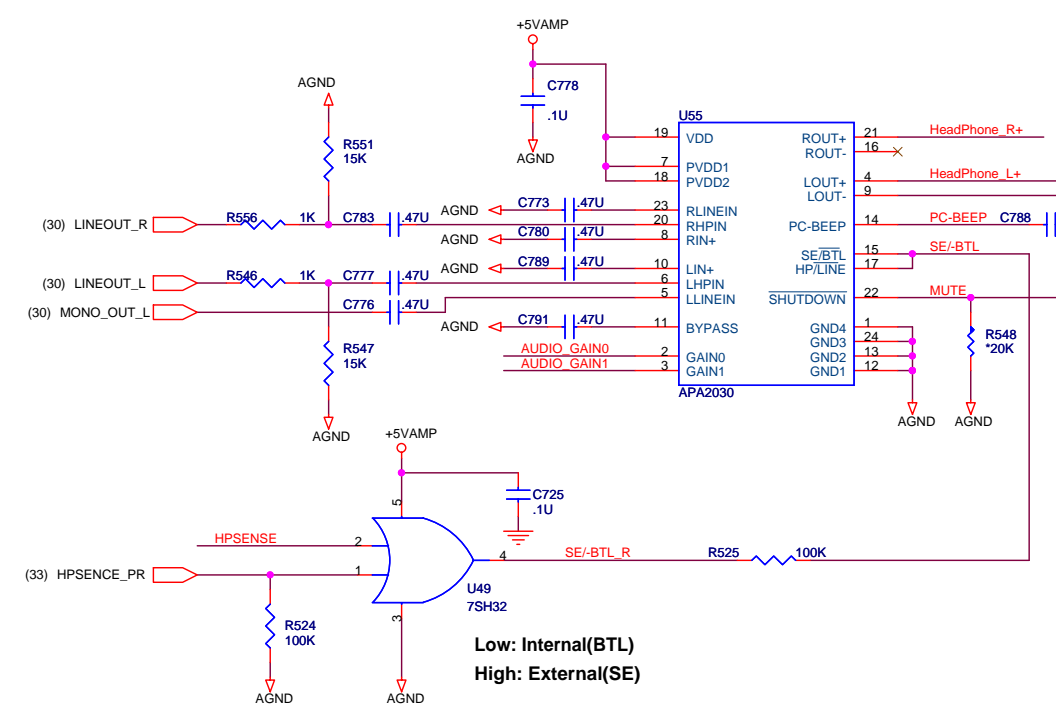
TRACK POINT CIRCUITS
MOVE TO PAGE 42



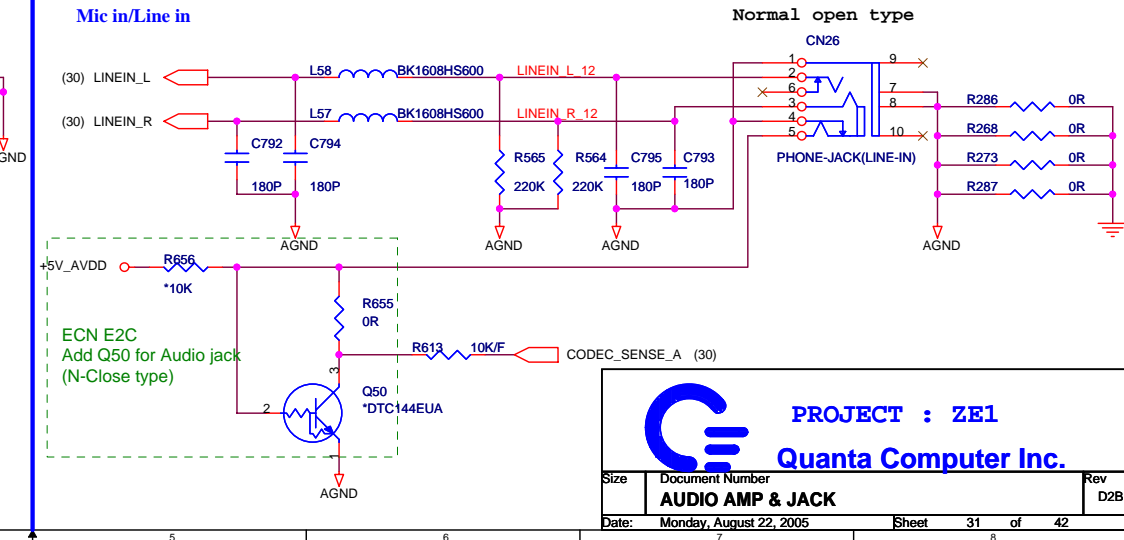
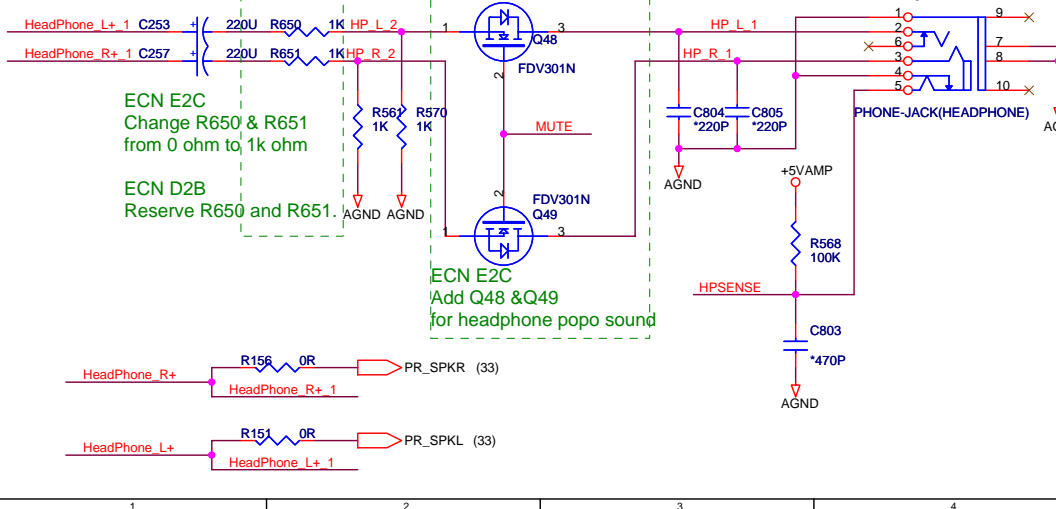
WWW.AliSaler.Com




Audio Amplifier



Head phone/Line out

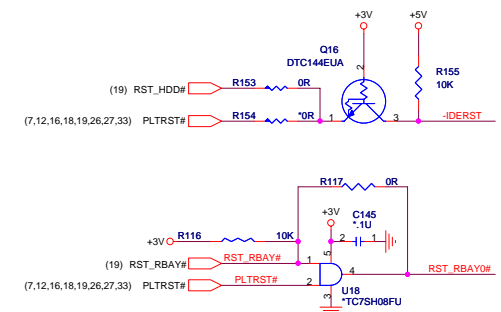
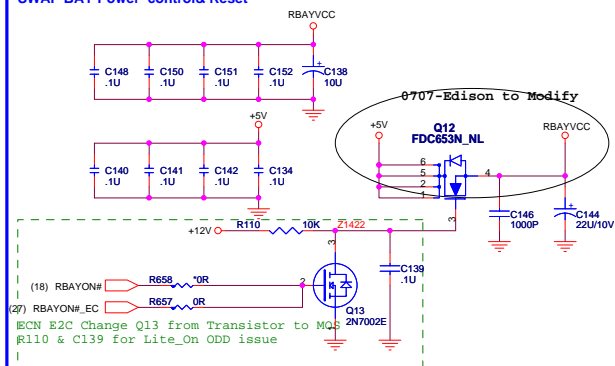




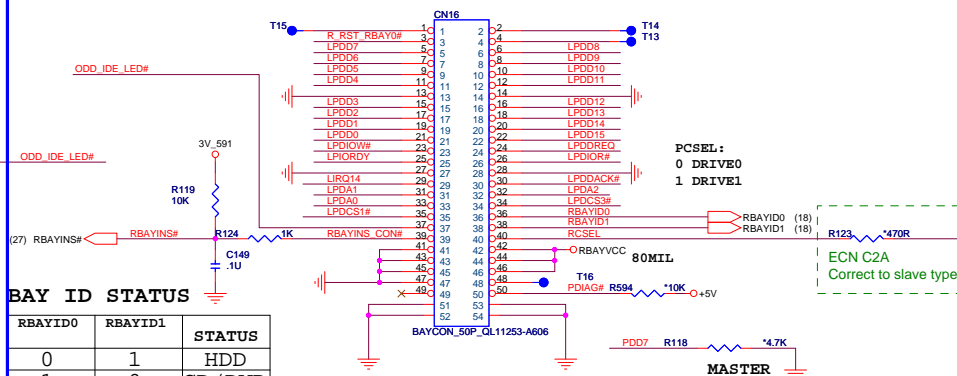
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Quanta Computer Inc.

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	AUDIO AMP & JACK	D2B
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IDE Reset



ODD CONN



EN C2A
Need change to Green color

IDE_LED#

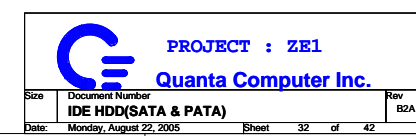
IDE_LED

LED4

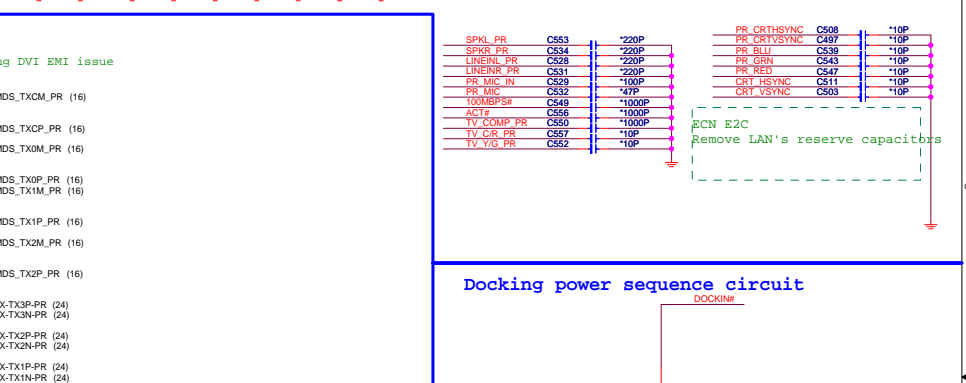
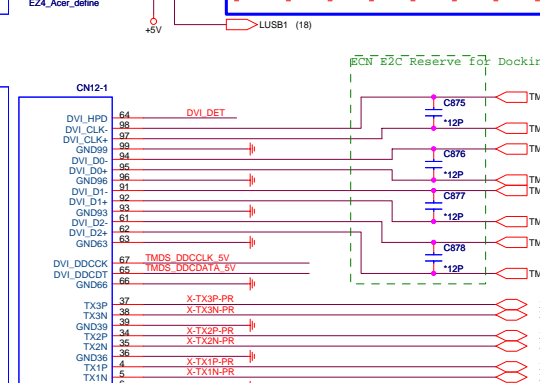
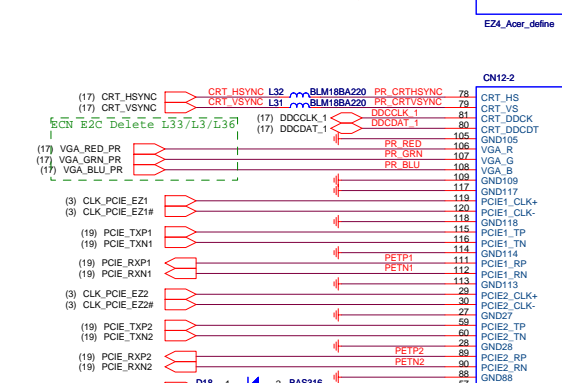
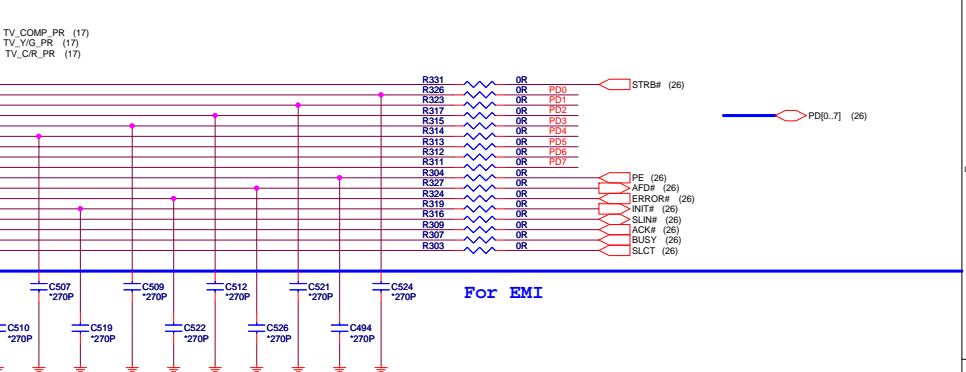
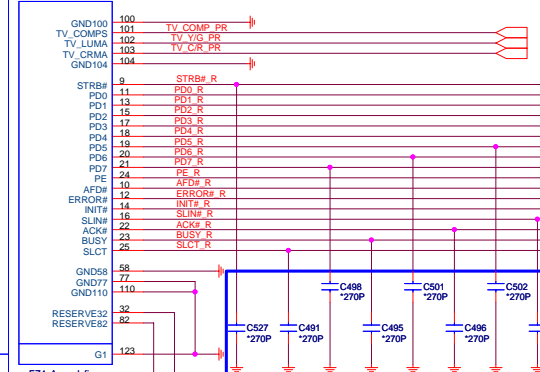
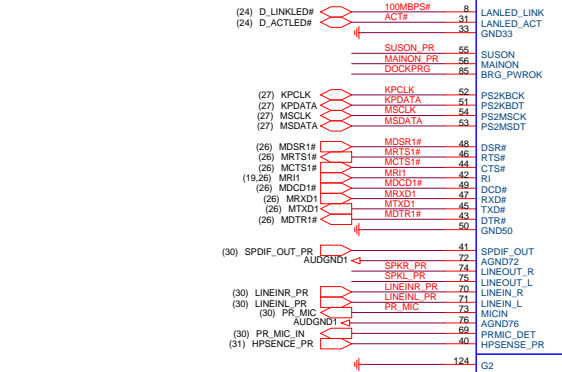
LED_GREEN

Q14

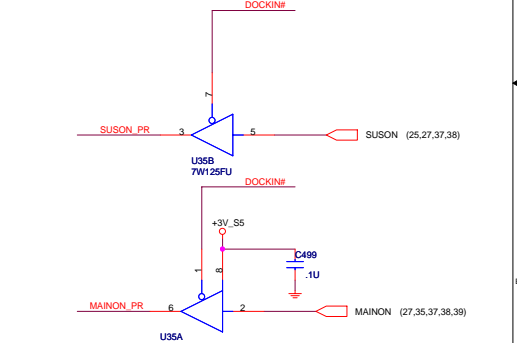
DTC14EUA



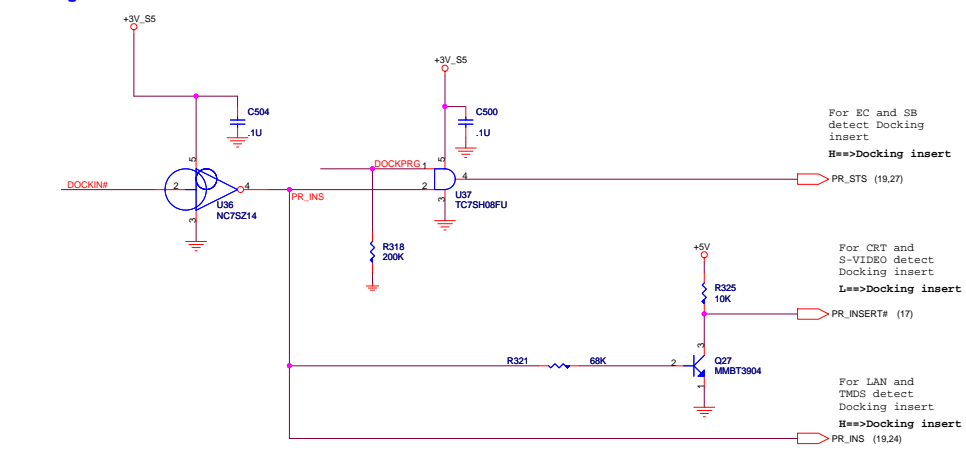
Docking Interface



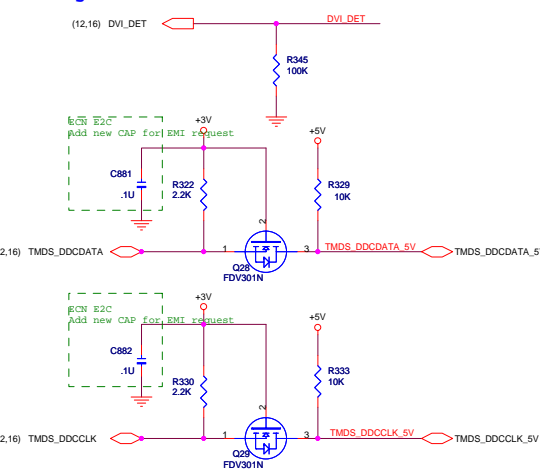
Docking power sequence circuit



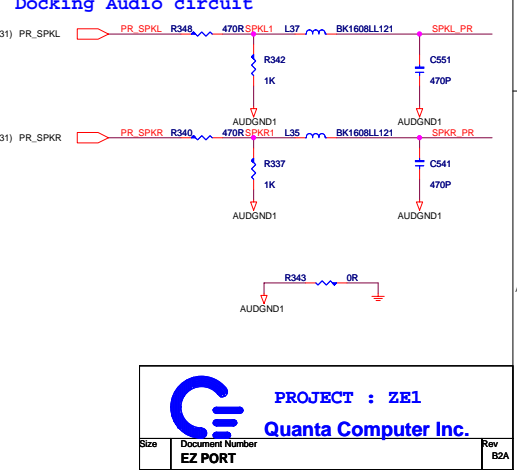
Docking Insert circuit



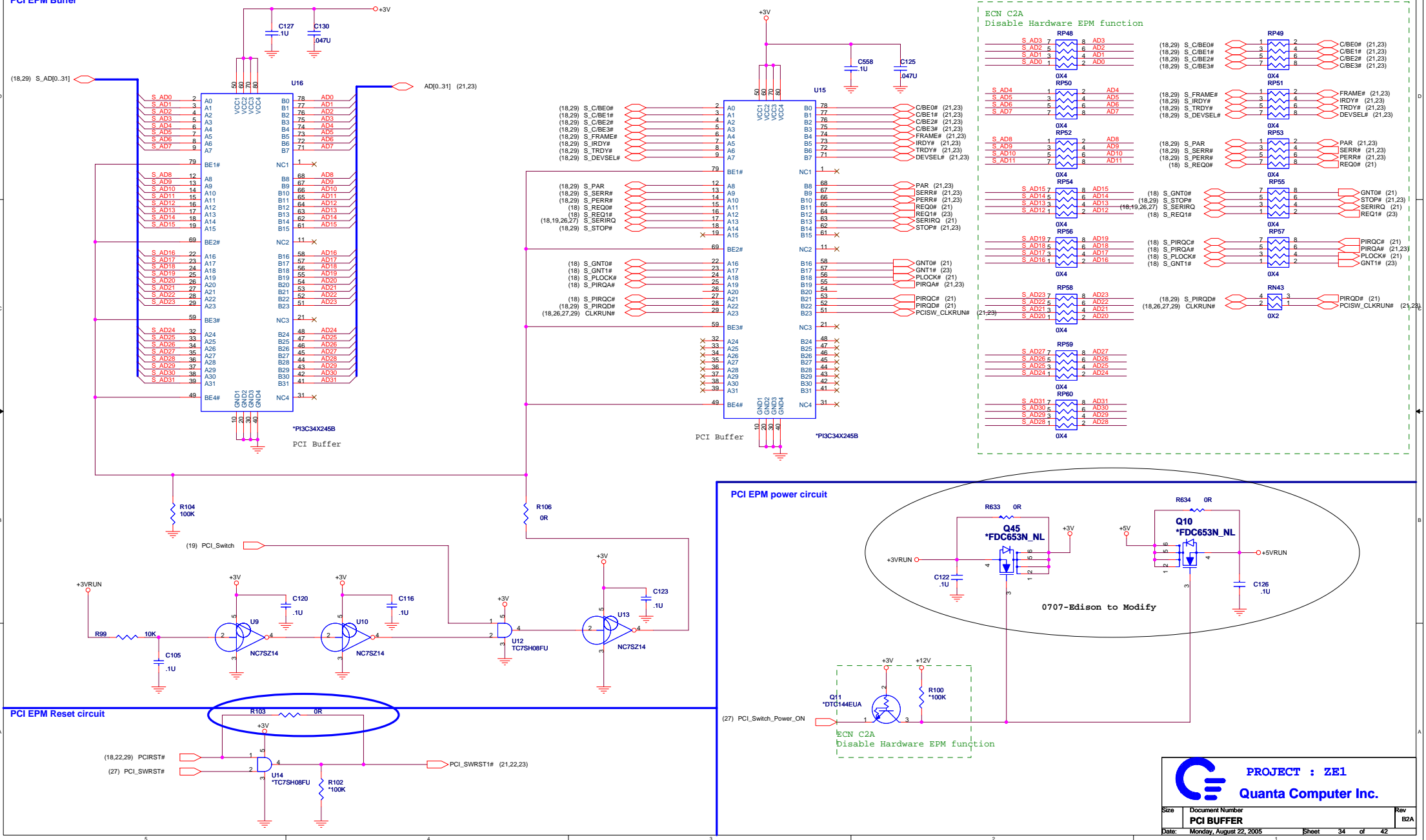
Docking TMDS circuit

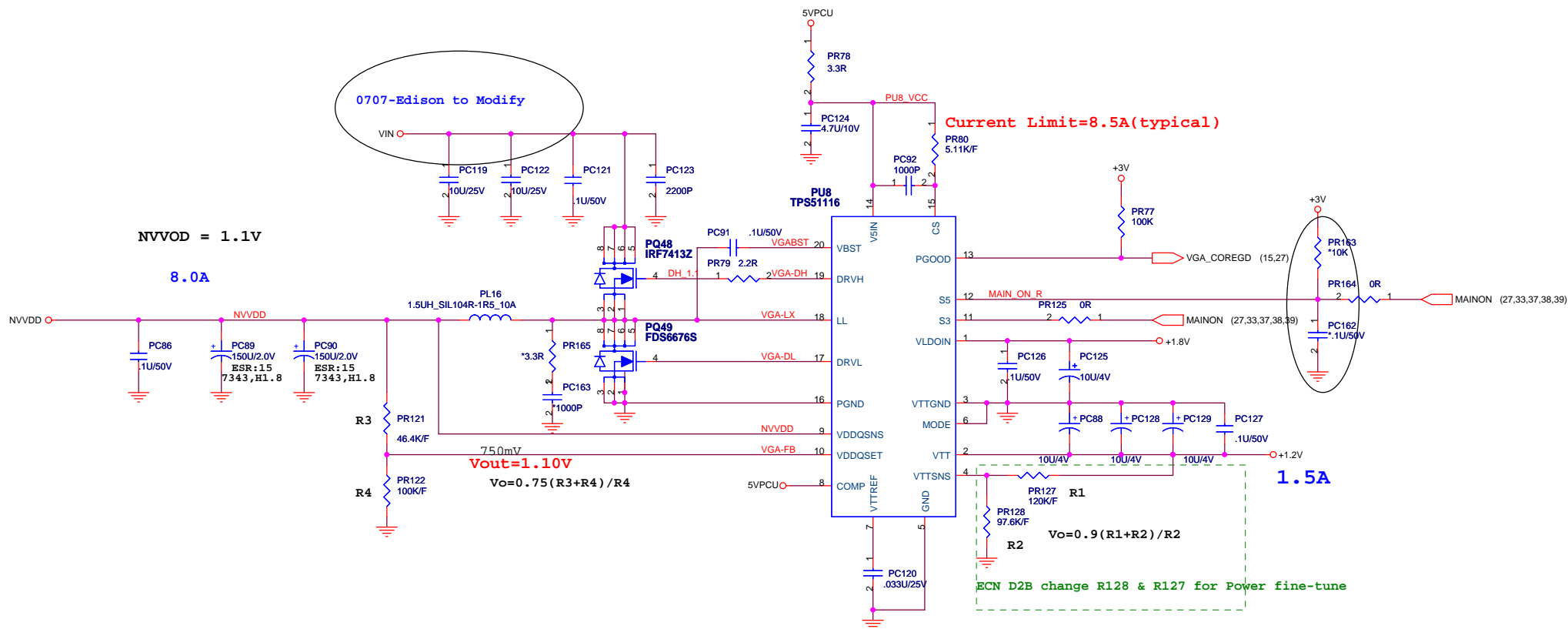


Dealing with the situation

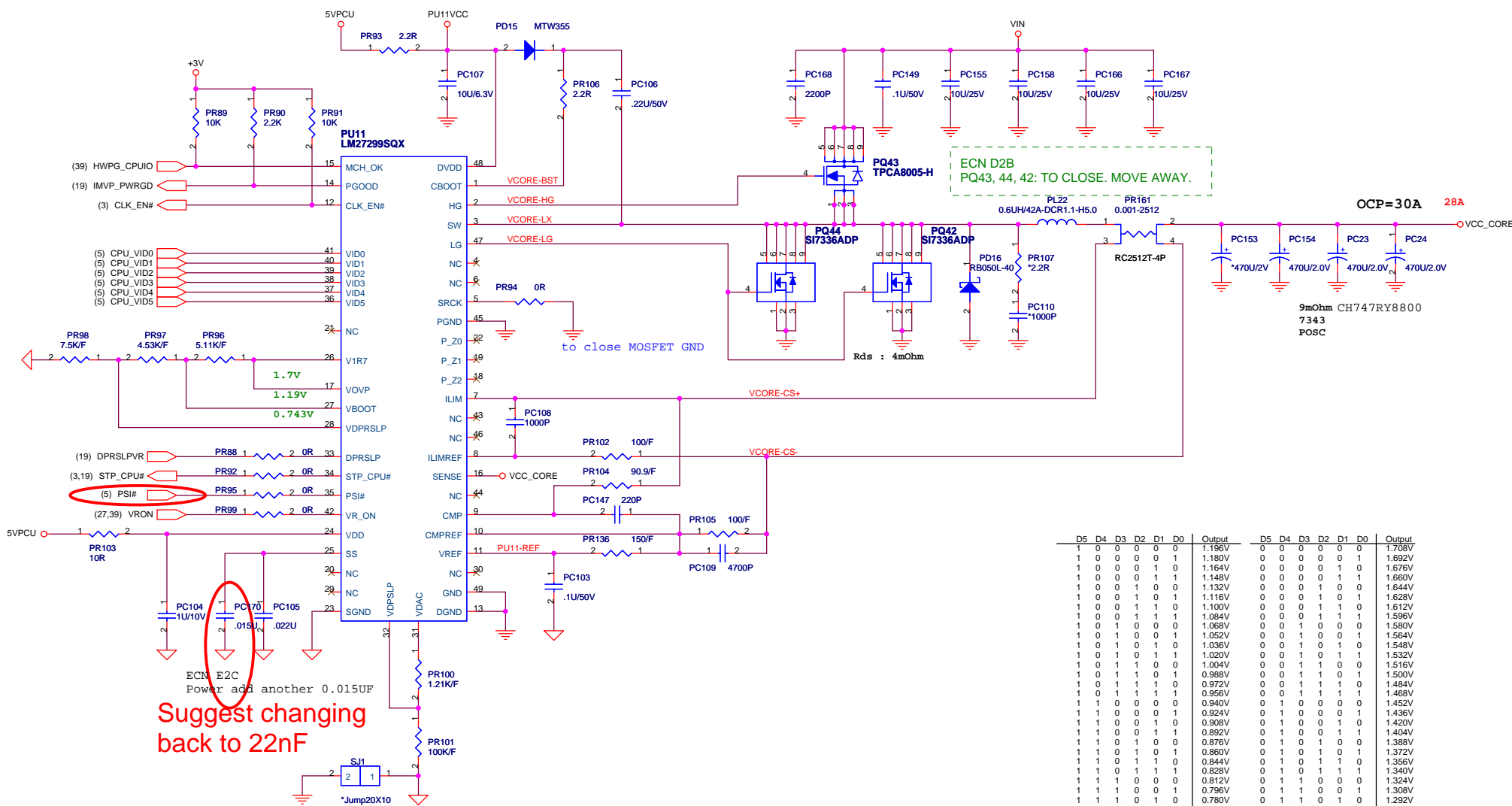


PCI EPM Buffer



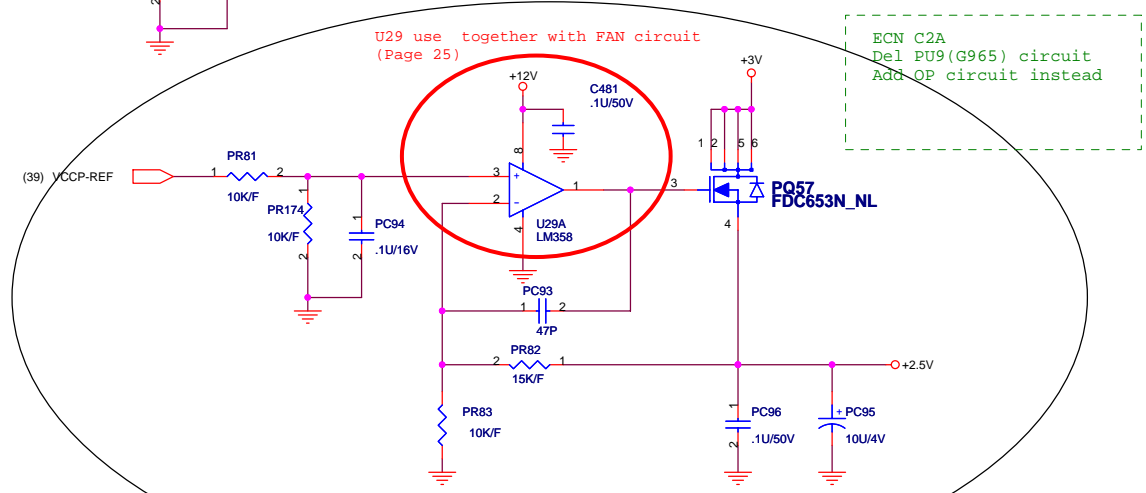
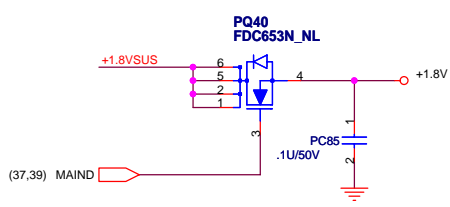
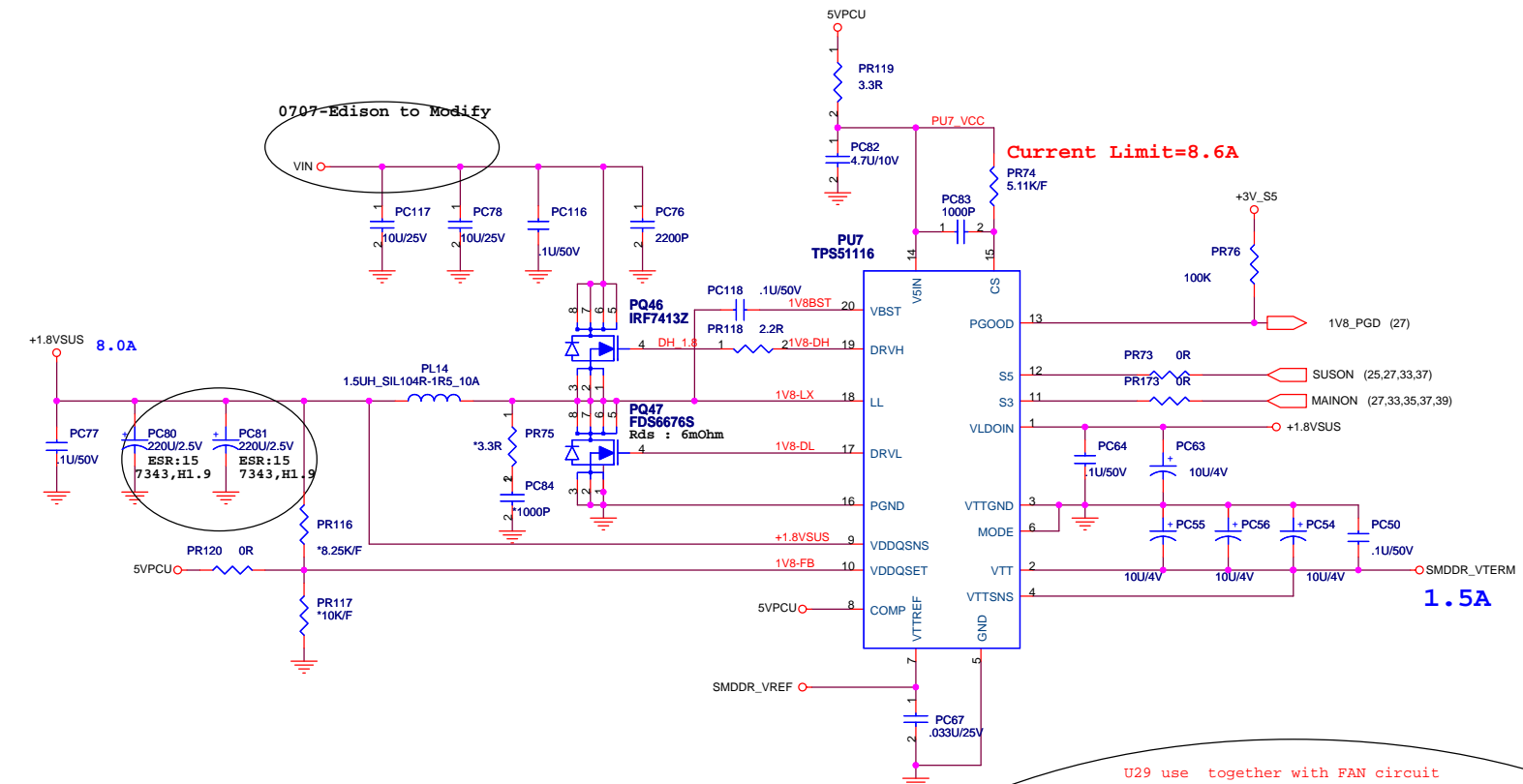


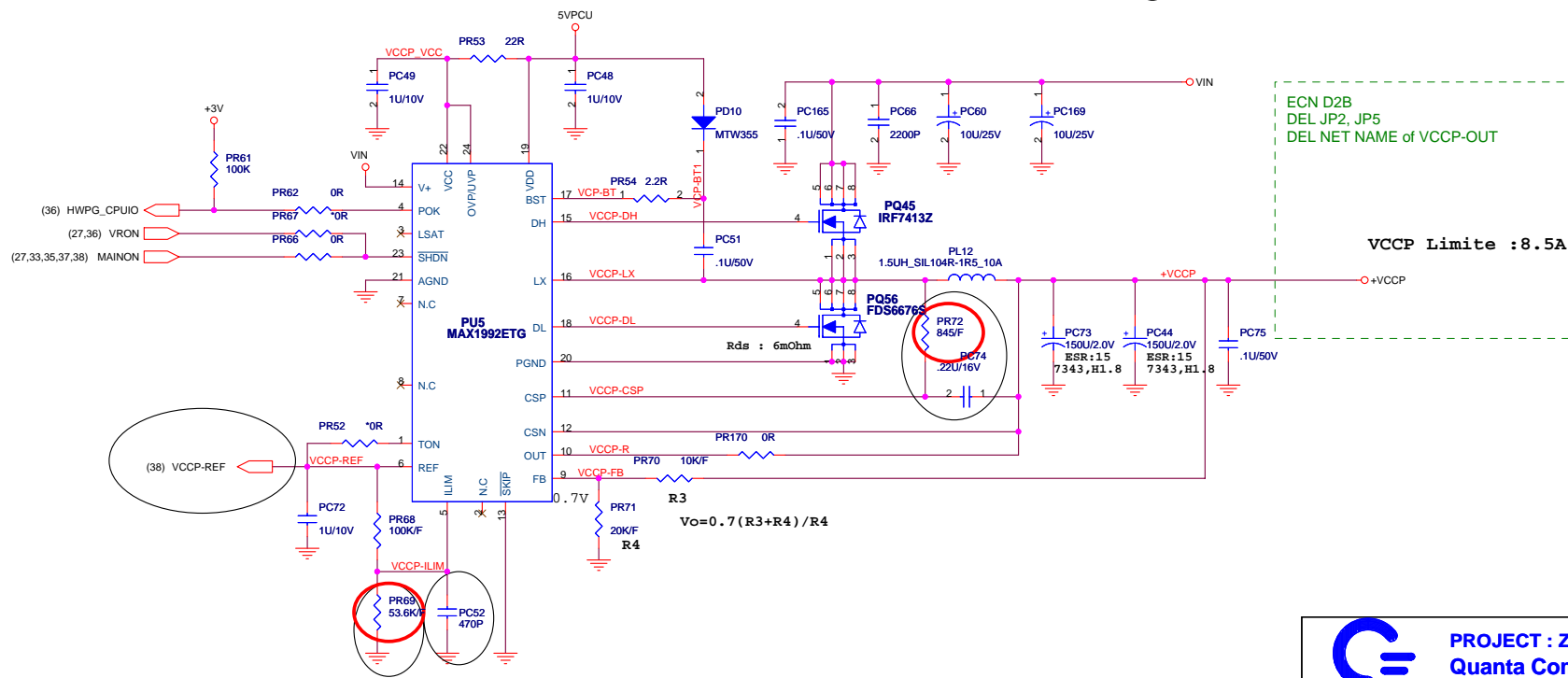
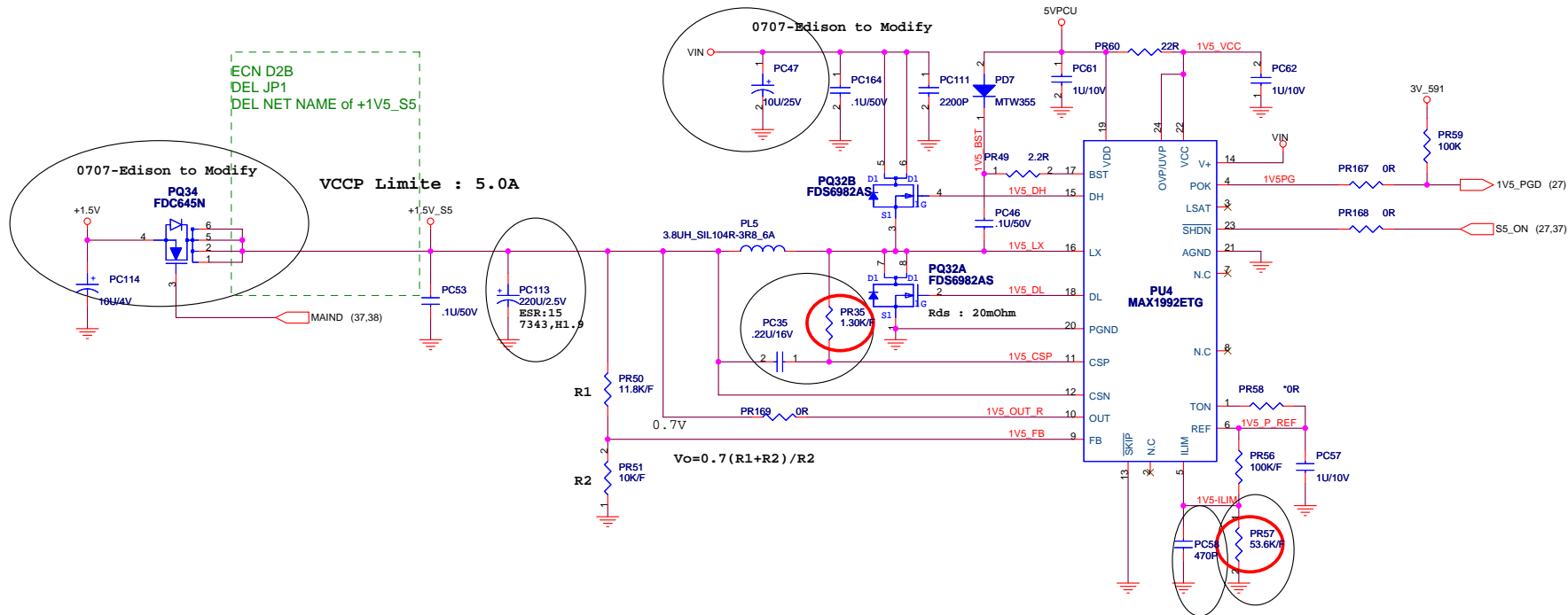
PROJECT : ZE1
Quanta Computer Inc.



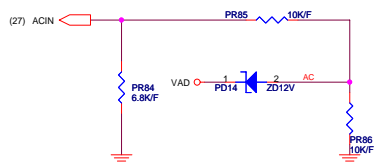
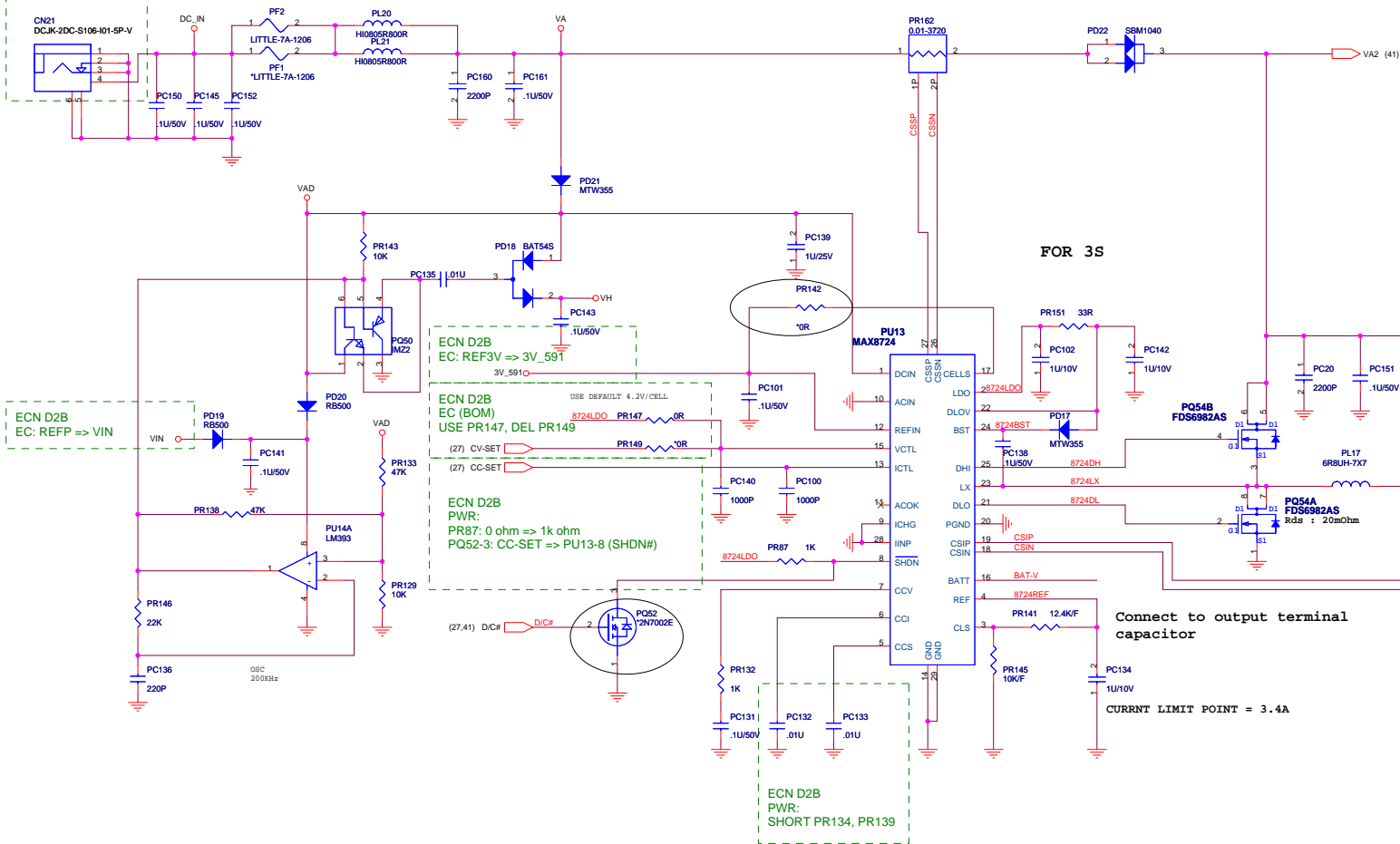
Suggest changing
back to 22nF

D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V	0	0	0	0	0	0	1.708V
1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	0	1	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	0	1	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	0	1	1	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	0	1.596V
1	0	1	0	0	0	1.068V	0	0	0	1	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	0	1	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	0	1	0	1	1.548V
1	0	1	0	1	1	1.020V	0	0	0	1	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	0	0	0	1.516V
1	0	1	1	0	1	0.988V	0	0	1	0	1	0	1.500V
1	0	1	1	1	0	0.972V	0	0	1	1	1	0	1.484V
1	0	1	1	1	1	0.956V	0	0	1	1	1	1	1.468V
1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
1	1	0	0	0	1	0.924V	0	1	0	0	0	1	1.436V
1	1	0	0	1	0	0.908V	0	1	0	0	1	0	1.420V
1	1	0	0	1	1	0.892V	0	1	0	0	1	1	1.404V
1	1	0	1	0	0	0.876V	0	1	0	1	0	0	1.388V
1	1	0	1	0	1	0.860V	0	1	0	1	0	1	1.372V
1	1	0	1	1	0	0.844V	0	1	0	1	1	0	1.356V
1	1	0	1	1	1	0.828V	0	1	0	1	1	1	1.340V
1	1	1	0	0	0	0.812V	0	1	1	0	0	0	1.324V
1	1	1	0	0	1	0.796V	0	1	1	0	0	1	1.308V
1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V





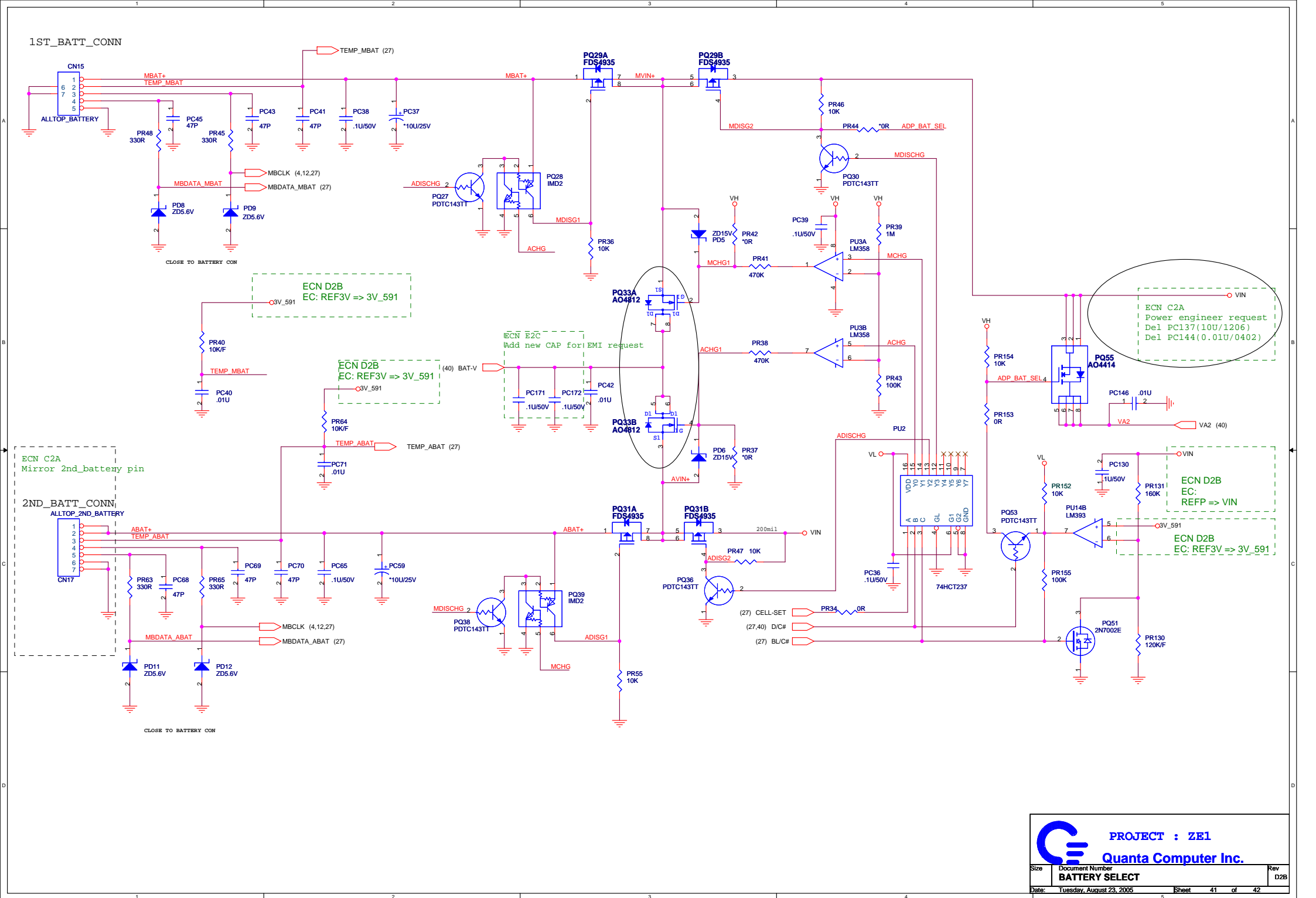
ECN D2B
ME/ PWR
UPDATE CN21



ECN D2B
EC: REF3V => 3V_591
EC: REFP => VIN
DEL PQ41, PU10, PC97, PC98, PC99



PROJECT : ZE1
Quanta Computer Inc.



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